

# **GEN series GEN17tA**

# Transient Recorder and Data Acquisition System

### **Special features**

- 19" rack mount system
- Up to 544 analog, 96 digital and 12 Timer/ Counter channels
- 100 MB/s to 175 MB/s of continuous streaming to a PC
- PTP time synchronization
- CAN/CAN FD output (option)
- EtherCAT® output (option)
- IRIG/GPS time synchronization (option)
- 1 Gbit optical Ethernet (option)
- 10 Gbit optical or electrical Ethernet with 400 MB/s continuous streaming (option)
- Removable SSD with 350 MB/s continuous streaming (option)
- Master/Sync connection (option)

The GEN17tA is a rack mountable transient recorder and data acquisition system for high-channel-count applications. If GEN7tA mainframes cannot handle the required channel count, the GEN17tA doubles the channel count reducing the overall installation costs.

The 1 Gbit Ethernet interface can stream recorded data directly to the PC at a rate of 100 MB with compression up to 175 MB/s. The optional 1 Gbit Optical Ethernet allows for isolated control of the mainframe and for cable lengths up to 10 km (6.2 mi) while maintaining full streaming performance. For higher streaming rates of 400 MB/s, a 10 Gbit electrical or optical Ethernet option is available. Maximum reliable data storage is achieved using (optional) built-in solid state drive at 350 MB/s.

Full transparent time and trigger synchronization on multiple GEN DAQ systems can be achieved by using the standard optical Master/Sync connector. To synchronize the absolute time to non GEN DAQ systems, GEN17tA supports the PTP protocol on both the standard and optional optical 1 Gbit Ethernet interfaces. The optional EtherCAT® or CAN/CAN FD output enables low latency real-time standalone data exchange. GEN17tA is configured and controlled using Perception software that runs on a PC provided by the user. This combination results in a sophisticated instrument for ultra-fast recording, analysis and reporting.



### **Supported Operation Mode** Recommended features Stand-alone Pre-configured boot GEN17tA settings Front View 350 MB/s storage to SSD Start/Stop/Trigger TTL inputs CAN FD acquisition control GEN DAQ API • Real-time formula database Figure 1.1: Stand-alone CAN-FD/EtherCAT® output • Wi-Fi access Single mainframe GEN17tA Start/Stop/Trigger TTL Rear View inputs CAN FD acquisition control 350 MB/s storage to SSD Real-time formula database CAN-FD/EtherCAT® output • PTP (GPS/IRIG) time sync Figure 1.2: Single mainframe Wi-Fi access **Dual mainframe** Single wire Master/Sync control UL-0265 PTP Optional 700 MB/s storage to SSD Network switch Master/Sync Real-time formula database connection CAN-FD/EtherCAT® output PTP (GPS/IRIG) time sync Corporate network Figure 1.3: Dual mainframe Four mainframes (>2) UL-0265 PTP Master/Sync control & Network switch trigger 1400 MB/s storage to SSD Real-time formula database CAN-FD/EtherCAT® output PTP (GPS/IRIG) time sync Corporate network Optional Master/ Sync connection Requires 1-G083 Figure 1.4: Multi mainframe Distributed mainframes (>2) Optical network Distributed data storage 1400 MB/s storage to SSD Real-time formula database UL-0265 PTP Network switch PTP (GPS/IRIG) time sync Corporate network Figure 1.5: Multi mainframe (distributed)

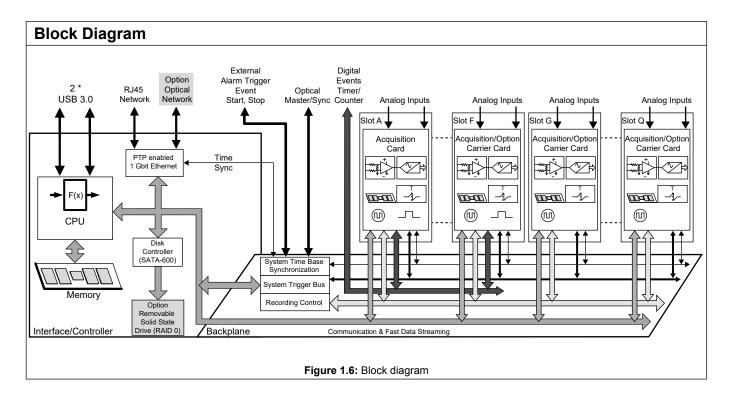
Mainframe to Mainframe Synchronization Options				
Network setup		Number of (mixed) GEN DAQ mainframes used		
	1	2	> 2	
Direct network to PC/Notebook	Not required	Use Master/Sync setup 1-G091 in both mainframes	Use Master/Sync setup 1-G083 in master mainframe 1-G091 in other mainframes	
Standard switch (No PTP support)	Not required	Use Master/Sync setup 1-G091 in both mainframes	Use Master/Sync setup 1-G083 in master mainframe 1-G091 in other mainframes	
PTP Network switch (e.g. HBK UL-0265)	Not required	Works for continuous recording No synchronized triggers for dual and sweep recording OR Use Master/Sync setup 1-G091 in both mainframe	Works for continuous recording No synchronized triggers for dual and sweep recording OR Use Master/Sync setup: 1-G083 in master mainframe 1-G091 in other mainframes	

## **Maximizing Continuous Data Recording Speed**

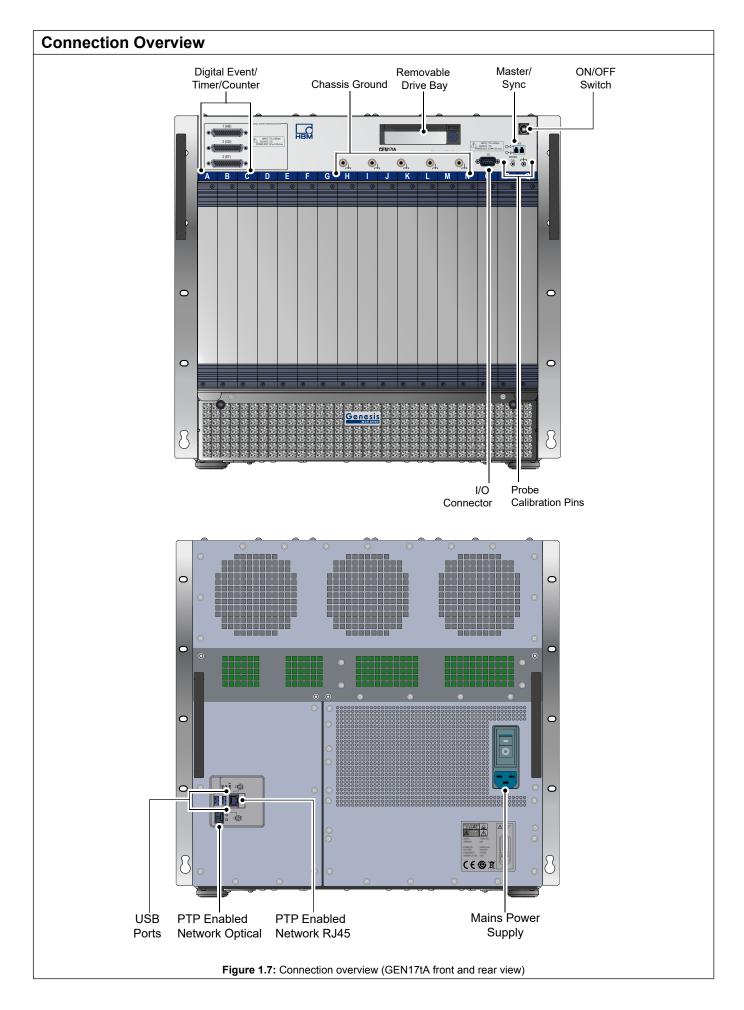
When using continuous data recording two elements in the setup typically impact the maximum speed: network and drive.

Both bottlenecks can be addressed by selecting the right setup. Either divide (multiple network cables or drives) the data load or increase the speed (10 Gbit ethernet and/or Solid State drives / RAID drives)

Network and/or drive setup		Number of (mixed) GEN DAQ mainframes used		
	1	2	>2	Notes
Direct 1 Gbit network to PC (no switch used) 100 MB/s per 1 Gbit network cable	100 MB/s	200 MB/s	3 MF: 300 MB/s 4 MF: 400 MB/s  10 MF: No support	The PC drive might limit the speed A network ports / PC will work Notebooks usually have 1 network port
1 Gbit network switch with 1 Gbit to PC 100 MB/s per 1 Gbit network cable	100 MB/s	100 MB/s	3 MF: 100 MB/s 4 MF: 100 MB/s  10 MF: 100 MB/s	<ul> <li>A single 1 Gbit cable to PC limits the speed</li> <li>Not preffered for continuous recording</li> </ul>
1 Gbit network switch with 10 Gbit to PC 100 MB/s per 1 Gbit network cable ~700 MB/s per 10 Gbit network cable	100 MB/s	200 MB/s	3 MF: 300 MB/s 4 MF: 400 MB/s  10 MF: 700 MB/s	<ul> <li>The PC drive might limit the speed</li> <li>10 Gbit on PC's is not yet standard</li> <li>Notebooks usually do not support</li> <li>10 Gbit</li> <li>A single 10 Gbit port reduces costs</li> </ul>
10 Gbit network switch with 10 Gbit to PC ~700 MB/s per 10 Gbit network cable	400 MB/s	700 MB/s	3 MF: 700 MB/s 4 MF: 700 MB/s  10 MF: 700 MB/s	<ul> <li>The PC drive might limit the speed</li> <li>10 Gbit on PC's is not yet standard</li> <li>Notebooks usually do not support</li> <li>10 Gbit</li> <li>Cost effective 10 Gbit switches exist</li> </ul>
Mainframe local disk storage 350 MB/s per Mainframe drive 1 Gbit network switch with 1 Gbit to PC	350 MB/s	700 MB/s	3 MF: 1050 MB/s 4 MF: 1400 MB/s  10 MF: 3500 MB/s	Worry free extreme reliable setup     Scales with every added mainframe     Low cost 1 Gbit switches can be used



Acquisition System	
System Time Base and Synchronization Central time base for all acquisition cards	
Accuracy	± 3.5 ppm; aging after 10 years ± 10 ppm
Base	Binary, Decimal or External
Synchronization sources	IEEE1588:2008 PTPv2 (Precision Time Protocol) using an End-to-End protocol Master/Sync; Sync or Master mode on built-in connector Master output card (G083): Option to synchronize up to 128 Sync mainframes
PTP synchronization accuracy	± 150 ns; with one PTP compatible Ethernet switch used When network switches are required, use only PTP IPv4 aware switches that support End- to-End set-ups. Overall accuracy depends on PTP switch used.  Note: PTP aware switches require PTP setup, refer to the operating manual of the switch for more details.
Acquisition Slots Unused slots must be covered using the GEN DA and also regulates the internal airflow to cool the	Q blind panel. This closes the mainframe front panels for EMC/EMI and safety compliance acquisition system correctly.
Number of slots	17
Acquisition cards	Any combination of GEN DAQ acquisition cards which support fast data streaming
Digital Event/Timer/Counter connector	3; Connected to slots A and B, C and D, E and F
Thermal control	Every acquisition card and the acquisition system monitors its own temperature and status. This is used to regulate fan speeds and reduce noise while optimizing airflow and power consumption.
Calibration	Any changes to the acquisition system configuration may change its internal thermal gradients. As accurate calibration relies on a steady and repeatable thermal environment, calibration is void if changes are made in the configuration. For information on calibration impact, please refer to the individual card specifications.



### 1 Gbit Network Interface

GEN17tA supports an electrical and optional optical 1 Gbit Ethernet connector

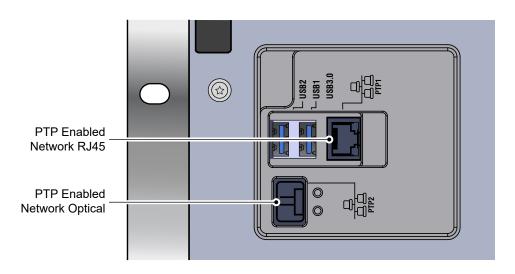


Figure 1.8: Electrical and optical 1 Gbit network interface

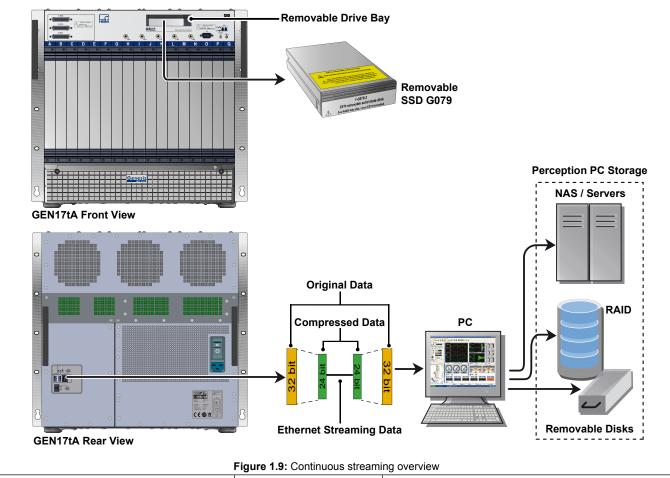
Standard Ethernet	1000BASE-T; 1 Gbit, CAT5e UTP or STP (RJ-45 connector)
Optical Ethernet	1000BASE-SX or 1000BASE-LX; 1 Gbit, Ethernet using optional SFP module
1000BASE-SX SFP (option G091)	850 nm, maximum 500 m Multi Mode 50/125 µm optical cable length, LC connector
1000BASE-LX SFP (option G063)	1310 nm, maximum 10 km Single Mode 9/125 µm optical cable length, LC connector
TCP/IP IPv4	
Address setup	DHCP/Auto IP or fixed IP
DHCP setup	When DHCP fails, APIPA (Automatic Private IP Addressing) is used similar to Windows® PCs
Gateway setup	Gateway setup supported for control using VPN and/or Internet
TCP/IP IPv6	Not supported
PTPv2 (IEEE1588:2008) synchronization	Supported on standard and optical 1 Gbit Ethernet interface (See table "Supported Acquisition Cards" for details)
Wake-on-LAN	Supported on standard and optical 1 Gbit Ethernet interface
Multiple Ethernet use cases	PTPv2 (IEEE1588:2008) can be used on separate (dedicated) Ethernet interface
Maximum Transfer Speed	
Continuous recording to a remote PC	100 MB/s <sup>(1)</sup> uncompressed, up to 175 MB/s with compression
CPU and Software	
CPU	Intel 6102E, Core™ i3 6th generation; 2 Core, 4 threads; 1.9 GHz
Operating System	Linux <sup>(2)</sup>
Linux boot drive	Non-removable built-in SSD; SSD cannot be used to store recorded data

<sup>(1)</sup> Tested using circular recording for 48 hours. Test setup uses a Windows® PC with Intel i7 CPU and SSD with sustained write speeds exceeding 250 MB/s.

<sup>(2)</sup> Linux GPL open source code can be downloaded from the HBM website.

### **GEN17tA Recorded Data Storage Overview**

GEN series mainframes support different ways of storing data. Continuous streaming throughput is tested by using 48 hours of circular recordings at specified data rates.



Maximum continuous data storage rates	Removable SSD G079	Perception PC storage	
(tested using full disk circular recording for 48 hours)	Uncompressed	Uncompressed	Compressed
1 Gbit Ethernet (optical or electrical)	n/a	100 MB/s <sup>(1)</sup>	Up to 175 MB/s <sup>(1)(2)</sup>
10 Gbit Ethernet (optical or electrical)	n/a	400 MB/s <sup>(3)</sup>	n/a
Removable drive bay	350 MB/s	Not usable	Not usable

- (1) Test setup uses a Windows® PC with Intel i7 CPU and SSD with sustained write speeds exceeding 250 MB/s.
- (2) Compression ratio is defined by the ADC channel width. For details, please refer to the "Streaming Compression Ratio" table (below). Rate is valid before decompressing storage data to maintain backward PNRF compatibility.
- (3) Test setup uses a Windows® PC with Intel i7 CPU and SSD with sustained write speeds exceeding 700 MB/s and a 10 Gbit Ethernet link

Analog Channel Streaming Compression Ratio			
Acquisition cards	Sample width	sion ratio	
		16 bit storage	32 bit storage
GN310B, GN311B	18 bits	1:1	1.75 : 1
GN610B, GN611B	18 bits	1:1	1.75 : 1
GN815, GN816	18 bits	1:1	1.75 : 1
GN840B, GN1640B	24 bits	1:1	1.33 : 1
GN1202B	14 bits	1:1	N/A
GN3210, GN3211	24 bits	1:1	1.33 : 1
GN8101B, GN8102B, GN8103B	14 bits	1:1	N/A

# **Master/Sync Connection**

GEN series mainframes support a Master/Sync connector. The connector can be used as a single Master output or as a Sync input. The Master output function can be extended using the Master output card (G083).

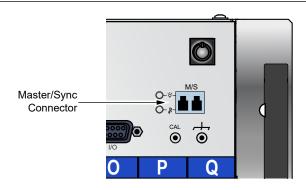
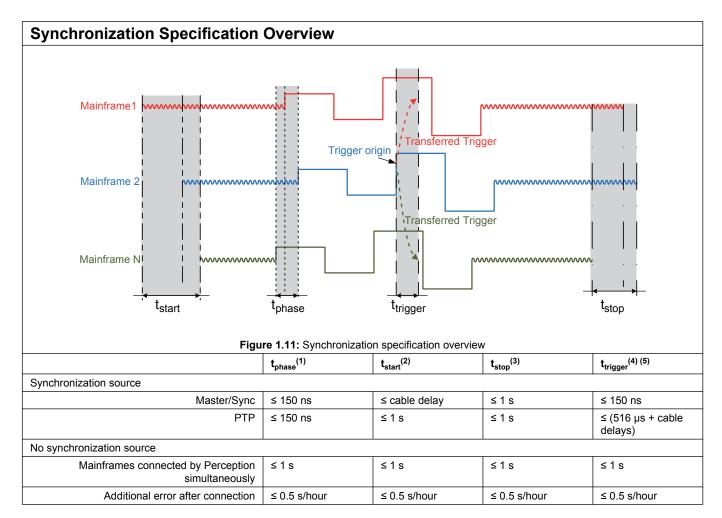


Figure 1.10: Master/Sync connector

Mainframe to mainframe phase shift	± 150 ns RMS
LED signaling	Optical link synchronized, not connected, function disabled
Master mode	Basic and extended synchronization supported; Supports one Sync mainframe.  Multiple Sync mainframes support by using one or more optional Master output cards (G083)
Sync mode	Basic and extended synchronization supported
Maximum number of mainframes	2; more mainframes supported when using one or more optional Master output cards (G083)
Time required to full synchronization after Master	Sync signal detected
No recording active	Typically 1 minute
Recording or pause active	1 minute and an additional 25 s per ms recording time deviation from Master time
User notifications while recording	Time marks on Master/Sync signal lost/restored and Master/Sync time synchronized
Basic synchronization	
Cable length propagation delay	Automatic cable length detection and propagation delay compensation
First sample	Synchronizes the first sample in a continuous recording for each mainframe. Cable length propagation delay not compensated at start of recording. First samples not recorded in the Sync mainframes, as defined by the propagation delays. Signal phase shifts are not introduced by this propagation delay.
Synchronized time base	Prevents frequency drift of the sample rates within each mainframe
Measured channel trigger exchange	Synchronously exchanges measured channel triggers connected to the Master/Sync trigger bus to/from each connected mainframe. Typically used for the sweep recording modes.
Compatibility	Basic synchronization features are backward compatible with GEN series Master/Sync card option for both Master and Sync modes
Extended synchronization	
Calculated channel trigger exchange	Additional trigger bus to synchronously exchange trigger conditions detected on real-time calculated (RTC) channels between mainframes. RTC channel triggers have a longer delay caused by the required calculation time prior to establishing a trigger.
Synchronous manual trigger	User action within Perception to trigger all mainframes synchronously
Synchronous recording actions	Start/Stop and Pause a recording across multiple mainframes, each of which is controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of two GEN DAQ mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typical Master/Sync setup would be to stop Perception on one system and use one instance of Perception application to control both systems.
Compatibility	Extended synchronization features are not supported by the legacy Master/Sync card option. A mixed system setup automatically works with basic synchronization.
Connection	
Optical wavelength	850 nm
Optical cable type	Multi Mode 50/125 μm
Optical data rate	2 Gbit/s
Maximum cable length	500 m
Connector type	Duplex LC

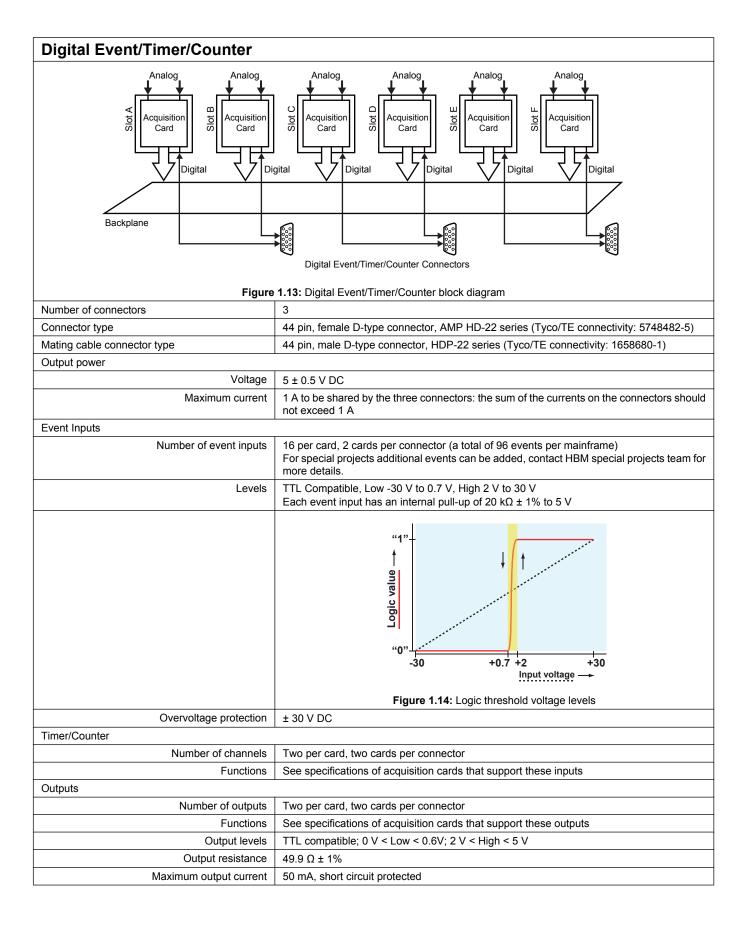


- (1) t<sub>phase</sub> Maximum phase difference between signals. (This specification is not affected by any of the other specifications).
- (2) t<sub>start</sub> Maximum delay between the start of recording for each mainframe.
- $(3) \qquad \textbf{t}_{\textbf{stop}} \qquad \text{Maximum delay between the stop of recording for each mainframe}.$
- (4)  $\mathbf{t}_{\text{trigger}}$  Maximum delay to transfer a trigger from one mainframe to all other mainframes.
- (5) Note on trigger exchange

Trigger exchange is included in the Master/Sync cable. All other synchronization modes require that the mainframes are connected from each External Trigger Out to each External Trigger In on all the mainframes in order to exchange triggers.

#### I/O Connector Signal PIN 1 - External Event In PIN 2 - External Event Out PIN 3 - External Trigger In PIN 4 - Ground PIN 5 - Ground PIN 6 - External Start In PIN 7 - External Trigger Out PIN 8 - External Stop In PIN 9 - +5V Figure 1.12: Pin assignment breakout cable TE (Tyco Electronics) connectivity: 2-5747706-0 (D-sub, 9-pin female) Connector type Mating connector type TE (Tyco Electronics) connectivity: 5-747904-5 1-KAB2132-0\_5: Breakout cable (Option, to be ordered separately) Cable type Coax Connector type 6; BNC female Length 0.5 m (1.6 ft) External input details (Trigger In / Event In / Start In / Stop In) Levels TTL compatible, Low -30 V to 0.7 V, High 2 V to 30 V Input has an internal pull-up of 20 k $\Omega$ ± 1% to 5 V Input overvoltage protection ± 25 V DC, ± 30 V peak <1 minute Resolution 50 ns Minimum pulse width filter $500 \text{ ns}, 1 \mu\text{s}, 2 \mu\text{s}, 5 \mu\text{s}, 10 \mu\text{s}$ Active edge Rising or falling; software selectable Delay ± 1 µs + up to one sample period Start response time Typically 1 s when system is completely idle Stop response time Typically 1 s when system is recording without automation External output details (Trigger out / Event out) TTL compatible; 0 V < Low < 0.6 V; 2 V < High < 5 V Levels Active level High/Low/Hold High; software selectable Pulse width High or Low selected: 12.5 to 12.8 µs Hold High selected: Active from first trigger to end of recording Maximum output current 50 mA, short circuit protected Output impedance 49.9 Ω ± 1% Short circuit protected Continuous External Trigger Out delay User selectable; minimum value may vary for each acquisition card. Default 516 $\pm$ 1 $\mu s$ + up to one sample period; Filter set to wideband $^{(1)}$ External Event Out delay User selected external trigger output delay - 1 µs

<sup>(1)</sup> If an analog and/or digital filter is used, extra delay will be added, depending on the type of filter and signal frequency.



### **Digital Event/Timer/Counter Connector Pin Assignment**



PIN 1 - Event Input A1/C1/E1 & Reset Timer/Counter A2/C2/E2 PIN 23 - Event Input B11/D11/F11 & Direction Timer/Counter B1/D1/F1 PIN 2 - Event Input A2/C2/E2 & Direction Timer/Counter A2/C2/E2 PIN 24 - Event Input B12/D12/F12 & Clock Timer/Counter B1/D1/F1 PIN 3 - Event Input A3/C3/E3 & Clock Timer/Counter A2/C2/E2 PIN 25 - Event Input B13/D13/F13 PIN 4 - Event Input A4/C4/E4 PIN 26 - Event Input B14/D14/F14 PIN 5 - Event Input A5/C5/E5 PIN 27 - Ground PIN 6 - Event Input A6/C6/E6 PIN 28 - Ground PIN 7 - Event Input A7/C7/E7 PIN 29 - Ground PIN 8 - Event Input A8/C8/E8 PIN 30 - Ground PIN 9 - Event Input A9/C9/E9 PIN 31 - Event Input B15/D15/F15 PIN 10 - Event Input A10/C10/E10 & Reset Timer/Counter A1/C1/E1 PIN 32 - Event Input B16/D16/F16 PIN 11 - Event Input A11/C11/E11 & Direction Timer/Counter A1/C1/E1 PIN 33 - Event Input A13/C13/E13 PIN 12 - Event Input A12/C12/E12 & Clock Timer/Counter A1/C1/E1 PIN 34 - Event Input A14/C14/E14 PIN 13 - Event Input B1/D1/F1 & Reset Timer/Counter B2/D2/F2 PIN 35 - Event Input A15/C15/E15 PIN 14 - Event Input B2/D2/F2 & Direction Timer/Counter B2/D2/F2 PIN 36 - Event Input A16/C16/E16 PIN 15 - Event Input B3/D3/F3 & Clock Timer/Counter B2/D2/F2 PIN 37 - Event Output B2/D2/F2 PIN 38 - Event Output B1/D1/F1 PIN 16 - Event Input B4/D4/F4 PIN 39 - Event Output A2/C2/E2 PIN 17 - Event Input B5/D5/F5 PIN 40 - Event Output A1/C1/E1 PIN 18 - Event Input B6/D6/F6 PIN 41 - Ground PIN 19 - Event Input B7/D7/F7 PIN 42 - Ground PIN 20 - Event Input B8/D8/F8 PIN 43 - +5 V Power PIN 21 - Event Input B9/D9/F9 PIN 22 - Event Input B10/D10/F10 & Reset Timer/Counter B1/D1/F1 PIN 44 - +5 V Power

Figure 1.15: Pin diagram for Digital Event/Timer/Counter connectors

Probe Calibration	
Pins	2; Signal and ground
Signal	~1 kHz square wave
Signal amplitude	0 V to 2 V using 1 M $\Omega$ load 0 V to 1 V using 50 $\Omega$ load

G085: GEN17tA Air Filter		
Filter type	UAF Qaudrafoam 0.25 inch/25PPI	
Synthetic Dust Weight Arrestance	Average 66% Tests performed in accordance with ASHRAE Standard 52.1 -1992 at 300 ft per minute (1.53 m/s) face velocity	
Air inlet	Air filter installed to filter inlet air	
Access	Easy access for cleaning and replacing air filter	





Figure 1.16: Easy access for cleaning/replacing air filter

Power	
Power Inlet	47-63 Hz, 100-240 V AC
Total Power of unit (maximum)	1200 VA

Dimensions  Height / Height with feet Width / Width including mounting ears Depth / Depth including handles Temperature Sensors  Cooling Fans Chassis ground Casing  A89 mm (19.2")  489 mm (19.2")  517 mm (20.4") / 557 mm (21.9")  518 mm (21.9")  519 mm (21.9")  510 mm (21.9")  510 mm (21.9")  511 mm (20.4") / 557 mm (21.9")  The total A-weighted SPL 61.5 dBA @ 0.6 m maximum  Temperature monitoring and air flow control  Cooling Fans Chassis ground Casing  Aluminum  489 mm (19.2")  557 mm (21.9")  557 mm (21.9")  557 mm (20.4")	/eight	
Height / Height with feet Width / Width including mounting ears Depth / Depth including handles The total A-weighted SPL 61.5 dBA @ 0.6 m maximum Temperature Sensors Temperature Sensors Tolique inlet, 3 @ outlet, 1 @ Linux PC, 2 @ Power supplies) all temperature regulated Chassis ground Casing  489 mm (19.2")  446 mm (17.5")  517 mm (20.4")  557 mm (21.9")  557 mm (21.9")	Mainframe	18.9 kg (41.67 lb), add ≈ 1 kg (2.2 lb) per acquisition card installed
Width / Width including mounting ears Depth / Depth including handles 517 mm (20.4") / 557 mm (21.9")  Acoustic Noise The total A-weighted SPL 61.5 dBA @ 0.6 m maximum  Temperature Sensors Temperature monitoring and air flow control  Cooling Fans 10 (4 @ inlet, 3 @ outlet, 1 @ Linux PC, 2 @ Power supplies) all temperature regulated chassis ground 5 Banana plugs (4 mm)  Aluminum  489 mm (19.2") 557 mm (21.9")  517 mm (20.4")	imensions	
Depth / Depth including handles 517 mm (20.4") / 557 mm (21.9")  Acoustic Noise The total A-weighted SPL 61.5 dBA @ 0.6 m maximum  Temperature Sensors Temperature monitoring and air flow control  200ling Fans 10 (4 @ inlet, 3 @ outlet, 1 @ Linux PC, 2 @ Power supplies) all temperature regulated 5 Banana plugs (4 mm)  Casing Aluminum  489 mm (19.2")  557 mm (21.9")  517 mm (20.4")	Height / Height with feet	444 mm (17.5") / 450 mm (17.7"), 10 height units of 19" rack
The total A-weighted SPL 61.5 dBA @ 0.6 m maximum Temperature Sensors Temperature monitoring and air flow control Cooling Fans 10 (4 @ inlet, 3 @ outlet, 1 @ Linux PC, 2 @ Power supplies) all temperature regulated Chassis ground Ch	Width / Width including mounting ears	446 mm (17.5") / 489 mm (19.2")
Temperature Monitoring and air flow control fooling Fans  10 (4 @ inlet, 3 @ outlet, 1 @ Linux PC, 2 @ Power supplies) all temperature regulated finassis ground fasing  Aluminum  489 mm (19.2")  557 mm (21.9")  517 mm (20.4")	Depth / Depth including handles	517 mm (20.4") / 557 mm (21.9")
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44 mm (17.5.))  A B C D E F G H I J K L M N O P D  A B C D E F G H I J K L M N O P D  A B C D E F G H I J K L M N O P D  A B C D E F G H I J K L M N O P D  A B C D E F G H I J K L M N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J K L M N N O P D  A B C D E F G H I J J K L M N N O P D  A	446 mm (17.5")	517 mm (20.4")
	W (17.5")  A B C D E F G H 1 J K 1  A B C D E F G H 2 K 1  A B C D E F G H 2 K 1  A B C D E F G H 3 K 1  A B C D E F G H 1 J K 1  A B C D E F G H 1 J K 1  A B C D E F G H 1 J K 1	

<b>Environmental Specifications</b>	
Temperature Range	
Operational	0 °C to +40 °C (+32 °F to +104 °F)
Non-operational (Storage)	-25 °C to +70 °C (-13 °F to +158 °F)
Thermal protection	Automatic shutdown above +40 °C (+104 °F) with notifications starting at +35 °C (+95 °F)
Relative humidity	0% to 80%; non-condensing; operational
Protection class	IP20
Altitude	Maximum 2000 m (6562 ft) above sea level; operational
Shock: IEC 60068-2-27	
Operational	Half-sine 5 g/11 ms; 3-axis, 1000 shocks in positive and negative direction
Non-operational	Half-sine 25 g/6 ms; 3-axis, 3 shocks in positive and negative direction
Vibration: IEC 60068-2-64	
Operational	1 g RMS, ½ h; 3-axis, random 5 to 500 Hz
Non-operational	2 g RMS, 1 h; 3-axis, random 5 to 500 Hz
Operational Environmental Tests	
Cold test IEC60068-2-1 Test Ad	-5 °C (+23 °F) for 2 hours
Dry heat test IEC-60068-2-2 Test Bd	+40 °C (+104 °F) for 2 hours
Damp heat test IEC60068-2-3 Test Ca	+40 °C (+104 °F), humidity > 93% RH for 4 days
Non-Operational (Storage) Environmental Tests	
Cold test IEC-60068-2-1 Test Ab	-25 °C (-13 °F) for 72 hours
Dry heat test IEC-60068-2-2 Test Bb	+70 °C (+158 °F) humidity < 50% RH for 96 hours
Change of temperature test IEC60068-2-14 Test Na	-25 °C to +70 °C (-13 °F to +158 °F) 5 cycles, rate 2 to 3 minutes, dwell time 3 hours
Damp heat cyclic test IEC60068-2-30 Test Db variant 1	+25 °C/+40 °C (+77 °F/+104 °F), humidity > 95/90% RH 6 cycles, cycle duration 24 hours

Harmonized Sta	andards for CE Compliance, According to the Following Directives	
Low Voltage Directive (L' Electromagnetic Compat	VD): 2014/35/EU ibility Directive (EMC): 2014/30/EU	
Electrical Safety		
EN 61010-1 (2010)	Safety requirements for electrical equipment for measurement, control, and laboratory use - General requirements	
EN 61010-2-030 (2010)	Particular requirements for testing and measuring circuits	
Electromagnetic Comp	atibility	
EN 61326-1 (2013)	Electrical equipment for measurement, control and laboratory use - EMC requirements - Part 1: General requirements	
Emission		
EN 55011	Industrial, scientific and medical equipment - Radio-frequency disturbance characteristics Conducted disturbance: class B; Radiated disturbance: class A	
EN 61000-3-2	Limits for harmonic current emissions: class D	
EN 61000-3-3	Limitation of voltage changes, voltage fluctuations and flicker in public low voltage supply systems	
Immunity		
EN 61000-4-2	Electrostatic discharge immunity test (ESD); contact discharge ± 4 kV/air discharge ± 8 kV: performance criteria B	
EN 61000-4-3	Radiated, radio-frequency, electromagnetic field immunity test; 80 MHz to 2.7 GHz using 10 V/m, 1000 Hz AM: performance criteria A	
EN 61000-4-4	Electrical fast transient/burst immunity test  Mains ± 2 kV using coupling network. Channel ± 2 kV using capacitive clamp: performance criteria B	
EN 61000-4-5	Surge immunity test  Mains ± 0.5 kV/± 1 kV Line-Line and ± 0.5 kV/± 1 kV/± 2 kV Line-earth Channel ± 0.5 kV/± 1 kV using coupling network: performance criteria B	
EN 61000-4-6	Immunity to conducted disturbances, induced by radio-frequency fields 150 kHz to 80 MHz, 1000 Hz AM; 10 V @ mains (coupling network), 10 V RMS @ channel (clamp); performance criteria A	
EN 61000-4-11	Voltage dips, short interruptions and voltage variations immunity tests Dips: performance criteria A; Interruptions: performance criteria C	

# G063: 1 Gbit Optical SFP Module Single Mode 1310 nm (Option, to be ordered separately)

Small Form-factor Pluggable (SFP) Optical transceiver used for:

Multi Mode 1310 nm 1 Gbit optical network support



#### **WARNING**

Use HBM approved transceivers only.



Data rate	1.0 Gbps	
Wavelength	1310 nm	
Input connector	LC	
Form factor	SFP	
Laser class	1	
Original manufacturer's part number	Foxconn AFCT-5710PZ	
Temperature Range		
Operational	-10 °C to +60 °C (-14 °F to +140 °F)	
Non-operational (Storage)	-40 °C to +85 °C (-40 °F to +158 °F)	

## G091: 2 Gbit Optical SFP Module Multi Mode 850 nm (Option, to be ordered separately)

Small Form-factor Pluggable (SFP) Optical transceiver used for:

- Multi Mode 850 nm 1 Gbit optical network support
- GN1202B optical front end connection
- GEN DAQ optical Master/Sync connections



### **WARNING**

Use HBM approved transceivers only.



Data rate	2.125 Gbps	
Wavelength	850 nm	
Input connector	LC	
Form factor	SFP	
Laser class	1	
Original manufacturer's part number	Finisar FTLF8519P3BNL	
Temperature Range		
Operational	-20 °C to +60 °C (-4 °F to +140 °F)	
Non-operational (Storage)	-40 °C to +85 °C (-40 °F to +158 °F)	

HBK: UNRESTRICTED 16 B04376\_03\_E00\_00

### G079: Removable Solid State Drive (Option, to be ordered separately)

SSD built inside protective drive carrier and configured in a RAID 0 setup. Drive carrier with SSD to be installed inside GEN7tA/GEN17tA drive bay. Used inside in the mainframe to secure data storage in the best way possible. Recorded data can be copied to a permanent archive using Perception software.





Figure 1.18: Example of SSD in drive carrier partially slid into GEN7tA (left) and SSD built inside drive carrier (right)

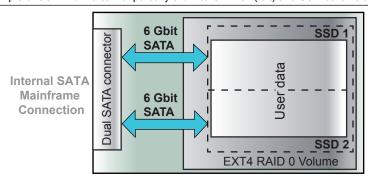


Figure 1.19: Block diagram drive

Storage configuration	
Storage technology	Solid State Drive (SSD)
Number of SSDs	2
SSD operation	RAID 0
EXT4 RAID 0 Volume unformatted size	960 GB
Maximum continuous storage speed	350 MB/s <sup>(1)</sup> when using SSDs that have been authorized for use by HBM
Maximum sweep storage speed	Depends on sweep length and number of channels used
File system format	Linux EXT4 Recorded data can be read, copied and deleted by Perception software that is connected to this GEN DAQ mainframe
Drive carrier configuration	
Hot Swap	Not supported, power off the GEN7tA/GEN17tA before adding/removing drive options
Minimum SATA speed	6 Gbit/s
SATA connectors	2; configured in RAID 0 setup
External USB-based carrier	Not supported due to the RAID 0 setup of the internal disks
Special configurations	
Using multiple G079 options	Multiple G079 SSD options can be ordered, only one G079 can be used at a time
RAID 1 configuration	Contact the local HBM support team to inquire about availability and to request a special project quote
Larger data drive	The size of SSDs increases almost every year. Contact the local HBM support team to inquire about availability and to request a project quote.

<sup>(1)</sup> Tested using circular recording with several combinations of acquisition cards for 48 hours.

# **G081: Option Carrier Card (Option, to be ordered separately)**

Used to enable optional synchronization and other interface cards. (See option card specifications for more details)

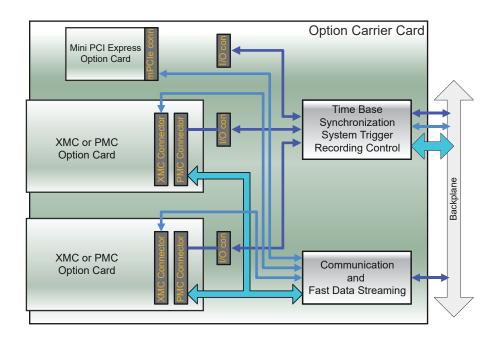


Figure 1.20: Block diagram option carrier card

rigate rizer blook diagram option carrier care		
Mainframe number of slots - 1 Every mainframe needs at least one acquisition card.		
GEN2tB, GEN3iA, GEN4tB, GEN7iA, GEN7tA and GEN17tA		
Two per option carrier card		
One per option carrier card		
1-G083 Master output card to support four Sync mainframes per Master output card Two Master output cards per option carrier card, multiple option carrier cards per mainframe		
1-G064 10 Gbit Ethernet card with SFP+ modules to support 850 nm and 1330 nm optical as well as RJ45 copper cable networks  One Ethernet option card per mainframe, cannot be combined with 1-G084		
1-G082 EtherCAT® card with configurable SDO and PDO data output (no setup) One EtherCAT® option card per mainframe EtherCAT® card not supported in GEN3iA and GEN7iA		
Custom special 2 channel CAN FD card, not supported in GEN3iA and GEN7iA Can only be ordered through custom systems at: <a href="mailto:customsystems@hbm.com">customsystems@hbm.com</a>		
0 °C to 40 °C (32 °F to 104 °F)		
-25 °C to +70 °C (-13 °F to +158 °F)		

HBK: UNRESTRICTED 18 B04376\_03\_E00\_00

# G082: EtherCAT® Real-Time Output Card (Option, to be ordered separately)(1)

Supports one EtherCAT® connection using RJ45 connectors (option carrier card required). Factory installed option.

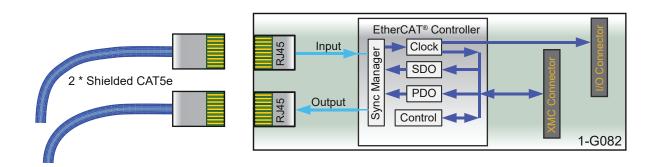


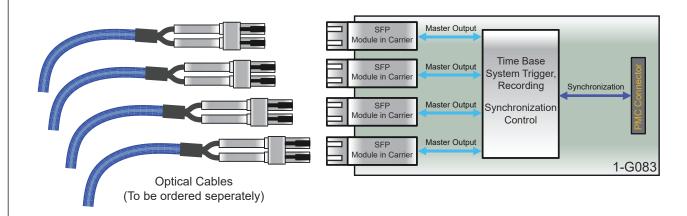
Figure 1.21: Block diagram EtherCAT® card

Required cables	Shielded CAT5e or similar <sup>(2)</sup>	
EtherCAT® Slave controller		
Туре	Beckhoff IP core	
Tested	Using Beckhoff master TwinCAT 3.1	
Fieldbus Memory Management Unit (FMMU)	4	
Sync managers	4	
ECS interface	2 x RJ45, 100BASE-TX, 100 MBit/s in accord	dance with IEEE-802.3, electrically isolated
LEDs	Error, Run Link/Activity for each channel	
Device profiles		
CANopen	Device profile supported	
Process Data Objects (PDO)		
DPRAM	60 kB	
Maximum update rate	1000 updates per second, typical latency 1 ms	
Dynamic mode	Variable ESI file dynamically configured with all published channels using the user defined channel names  Dynamic channel count up to 240 channels	
Static mode	Predefined ESI file, static configuration with a predefined channel names Fixed channel count options: 50, 100 or 200	
ESI file	Perception can generate the ESI file for the selected configuration	
Tested master configurations	Vendor	Master/application
	AVL	Puma
	Beckhoff	Twincat
	Intest	Inova
	Kratzer	PATools
	Kristl & Seibt	Tornado
	König PA	EtherCAT® Studio
	МАНА	MAHA RT
	National Instruments	Veristand
	D2T	Morpheé
Temperature Range		
Operational	0 °C to 40 °C (32 °F to 104 °F)	
Non-operational (Storage)	-25 °C to +70 °C (-13 °F to +158 °F)	

- (1) EtherCAT® is a registered trademark and patented technology, licensed by Beckhoff Automation GmbH, Germany.
- (2) For more details about the cables, please refer to the "EtherCAT\_DesignGuide\_en.pdf" from Beckhoff (www.beckhoff.com).

### G083: Master Output Card (Option, to be ordered separately)

Supports up to four Sync mainframes, multiple Master output cards supported (G081 option carrier card required). Factory installed option.



the first mainframe slots can be filled with option carrier cards.  Mainframe to mainframe phase shift  ± 150 ns RMS; measured on analog signals using identical acquisition modules, ide sample rates and filter settings in each mainframe  LED signaling  Optical link synchronized, not connected, function disabled  Basic and extended synchronization supported; four Sync mainframes per Master or card  Two Master output cards per option carrier card, multiple option carrier cards per main  Sync mode  Not supported. Use Master/Sync synchronization connector of mainframe for Sync re  Maximum mainframes  GENZIB: 9 Sync mainframes, 10 including Master mainframe  GENXIB: 9 Sync mainframes, 26 including Master mainframe  GENXIB: 9 Sync mainframes, 10 including Master mainframe  GENXIB: 3 Sync mainframes, 10 including Master mainframe  GENXIB: 9 Sync mainframes, 10 including Master mainframe  GENXIB: 9 Sync mainframes, 10 including Master mainframe  GENXIB: 10 Sync mainframes, 10 including Master mainframe  Time required to full synchronization after Master/Sync signal detected  No recording active  1 minute typical  Recording or pause active  1 minute plus 25 s per ms recording time deviation from Master time  User notifications while recording  Time marks on Master/Sync signal lost/restored and Master/Sync time synchronized  Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay  ±5 ns/m; Automatic cable length detection and propagation delay compensation  Synchronizes the first sample in a continuous recording for each mainframe. First same are not recorded in the Sync mainframes defined by the cable length propagation de Signal phase shifts are not introduced by this propagation delay.  Prevents frequency drift of the sample rates within each mainframe. First same are not recor	Figure 1.22: Block diagram Master output card (G081 required)		
LED signaling Optical link synchronized, not connected, function disabled Basic and extended synchronization supported; four Sync mainframes per Master or card Two Master output cards per option carrier card, multiple option carrier cards per main Sync mode Not supported. Use Master/Sync synchronization connector of mainframe for Sync r GEN2IB: 9 Sync mainframes, 10 including Master mainframe GEN2IB: 9 Sync mainframes, 26 including Master mainframe GEN3I, GEN3IA and GEN3I: 17 Sync mainframes, 18 including Master mainframe GEN7I, GEN7IA and GEN7IA: 49 Sync mainframes, 50 including Master mainframe GEN7I, GEN7IA and GEN7IA: 49 Sync mainframes, 50 including Master mainframe GEN7I, CEN7IA and GEN7IA: 49 Sync mainframes, 50 including Master mainframe GEN7I, CEN7IA and GEN7IA: 49 Sync mainframes, 50 including Master mainframe Time required to full synchronization after Master/Sync signal detected No recording active In minute typical Recording or pause active In minute typical In marks on Master/Sync signal lost/restored and Master/Sync time synchronized Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay First sample Synchronized time base Neasured channel trigger exchange Measured channel trigger exchange Measured channel trigger exchange Synchronized time base Prevents frequency drift of the sample rates within each mainframe Synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange Synchronously exchanges measured channel triggers connected to the Master/Sync triggers hat were caused by the mathematics prior to establishing a trigger.  Synchronous recording actions Synchronous recording actions Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC chriggers that were caused by the mathematics prior to establishing a trigger.  Synchronous recording actions Synchronously records dis	Master outputs	Four per Master output card. Up to two Master output cards per option carrier card. All but the first mainframe slots can be filled with option carrier cards.	
Basic and extended synchronization supported; four Sync mainframes per Master or card Two Master output cards per option carrier card, multiple option carrier cards per main Two Master output cards per option carrier card, multiple option carrier cards per main Two Master output cards per option carrier card, multiple option carrier cards per main Two Master mainframes (Part 1997). Two Maximum mainframes (Part 1997). Sync mainframes, 10 including Master mainframe (BCN3), GEN3/A and GEN3t. 17 Sync mainframes, 13 including Master mainframe (BCN3), GEN3/A and GEN3t. 17 Sync mainframes, 13 including Master mainframe (BCN17), GEN7/A and GEN7/A: 49 Sync mainframes, 15 including Master mainframe (BCN17), GEN7/A and GEN7/A: 49 Sync mainframes, 15 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes, 130 including Master mainframe (BCN17), GEN7/A: 129 Sync mainframes (BCN17), GEN7/A: 129 Sync mainframes deviation from Master time (BCN17), GEN7/A: 129 Synchronized and Master/Sync and option) (BCN17), All Master/Sync and option) (BCN17), All Master/Sync and option) (BCN17), All Master/Sync and propagation delay compensation delay compensation delay somensation (BCN17), All Master/Sync and option) (BCN17), All Master/Sync and option and option and option and option and option and option	Mainframe to mainframe phase shift	± 150 ns RMS; measured on analog signals using identical acquisition modules, identical sample rates and filter settings in each mainframe	
Sync mode  Not supported. Use Master/Sync synchronization connector of mainframe for Sync node  Maximum mainframes  GEN2IB: 9 Sync mainframes, 10 including Master mainframe GEN4IB: 25 Sync mainframes, 26 including Master mainframe GEN4IB: 25 Sync mainframes, 26 including Master mainframe GEN4IB: 25 Sync mainframes, 26 including Master mainframe GEN4II, GEN7IA and GEN7IA: 149 Sync mainframes, 50 including Master mainframe GEN17IA: 129 Sync mainframes, 130 including Master mainframe  Time required to full synchronization after Master/Sync signal detected  No recording active  1 minute typical  Recording or pause active  1 minute plus 25 s per ms recording time deviation from Master time  User notifications while recording  Time marks on Master/Sync signal lost/restored and Master/Sync time synchronized  Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay  First sample  First sample  Synchronizes the first sample in a continuous recording for each mainframe. First sa are not recorded in the Sync mainframes defined by the cable length propagation delay.  Synchronized time base  Prevents frequency drift of the sample rates within each mainframe  Synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange  Synchronously exchanges measured channel triggers connected to the Master/Sync to bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange  Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC chriggers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  Synchronous manual trigger action within Perception to trigger all mainframes synchronously separate instance of Perception. Stop recording is a non	LED signaling	Optical link synchronized, not connected, function disabled	
Maximum mainframes  GEN2tB: 9 Sync mainframes, 10 including Master mainframe GEN4tB: 25 Sync mainframes, 26 including Master mainframe GEN3t, GEN3t, and GEN3t: 149 Sync mainframes, 50 including Master mainframe GEN7ti, GEN7tA and GEN7tA: 49 Sync mainframes, 50 including Master mainframe GEN7ti, GEN7tA and GEN7tA: 49 Sync mainframes, 50 including Master mainframe GEN7ti, GEN7tA and GEN7tA: 49 Sync mainframes, 50 including Master mainframe GEN7ti, GEN7tA and GEN7tA: 49 Sync mainframes, 50 including Master mainframe  Time required to full synchronization after Master/Sync signal detected  No recording active 1 minute typical Recording or pause active 1 minute plus 25 s per ms recording time deviation from Master time User notifications while recording Time marks on Master/Sync signal lost/restored and Master/Sync time synchronized Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay  £5 ns/m; Automatic cable length detection and propagation delay compensation Synchronizes the first sample in a continuous recording for each mainframe. First sa net recorded in the Sync mainframes defined by the cable length propagation delay.  Synchronized time base Prevents frequency drift of the sample rates within each mainframe Measured channel trigger exchange Synchronously exchanges measured channel triggers connected to the Master/Sync to be between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changers that were caused by the mathematics prior to establishing a trigger.  User action within Perception to trigger all mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data	Master mode	Basic and extended synchronization supported; four Sync mainframes per Master output card Two Master output cards per option carrier card, multiple option carrier cards per mainframe	
GEN4tB: 25 Sync mainframes, 26 including Master mainframe GEN3i, GEN7iA and GEN7ti. 17 Sync mainframes, 18 including Master mainframe GEN7i, GEN7iA and GEN7ti. 49 Sync mainframes, 50 including Master mainframe GEN7iA: 129 Sync mainframes, 130 including Master mainframe GEN7iA: 129 Sync mainframes, 130 including Master mainframe Time required to full synchronization after Master/Sync signal detected  No recording active Inimute typical Recording or pause active Inimute plus 25 s per ms recording time deviation from Master time User notifications while recording Time marks on Master/Sync signal lost/restored and Master/Sync time synchronized Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay First sample Synchronizes the first sample in a continuous recording for each mainframe. First sa re not recorded in the Sync mainframes defined by the cable length propagation delay.  Synchronized time base Prevents frequency drift of the sample rates within each mainframe Measured channel trigger exchange Synchronously exchanges measured channel triggers connected to the Master/Sync bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger User action within Perception to trigger all mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application.	Sync mode	Not supported. Use Master/Sync synchronization connector of mainframe for Sync mode.	
No recording active Recording or pause active Recording or pause active User notifications while recording Time marks on Master/Sync signal lost/restored and Master/Sync time synchronized Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay First sample Synchronizes the first sample in a continuous recording for each mainframe. First sa are not recorded in the Sync mainframes defined by the cable length propagation delay.  Synchronized time base Prevents frequency drift of the sample rates within each mainframe Synchronously exchanges measured channel triggers connected to the Master/Sync bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changes that were caused by the mathematics prior to establishing a trigger.  Synchronous recording actions Synchronous recording actions Synchronous recording actions Synchronously records distributed data with a mix of GENTIA/GEN3iA mainframes in Master/Sync setup would be to control both systems from one Perception application.	Maximum mainframes	GEN4tB: 25 Sync mainframes, 26 including Master mainframe GEN3i, GEN3iA and GEN3t: 17 Sync mainframes, 18 including Master mainframe GEN7i, GEN7iA and GEN7tA: 49 Sync mainframes, 50 including Master mainframe	
Recording or pause active User notifications while recording Time marks on Master/Sync signal lost/restored and Master/Sync time synchronized Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay  £5 ns/m; Automatic cable length detection and propagation delay compensation  Synchronizes the first sample in a continuous recording for each mainframe. First sa are not recorded in the Sync mainframes defined by the cable length propagation delay.  Synchronized time base  Prevents frequency drift of the sample rates within each mainframe  Measured channel trigger exchange  Synchronously exchanges measured channel triggers connected to the Master/Sync to bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange  Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC ch triggers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  Synchronous recording actions  Start/Stop and Pause a recording across multiple mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GENTIA/GEN3iA mainframes in Master/Sync setup would be to control both systems from one Perception application.	Time required to full synchronization after Master/Sync signal detected		
User notifications while recording  Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay  First sample  First sample  Synchronizes the first sample in a continuous recording for each mainframe. First sample of signal phase shifts are not introduced by this propagation delay.  Prevents frequency drift of the sample rates within each mainframe  Measured channel trigger exchange  Measured channel trigger exchange  Synchronously exchanges measured channel triggers connected to the Master/Sync to bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange  Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  User action within Perception to trigger all mainframes synchronously a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application.	No recording active	1 minute typical	
Basic synchronization (backward compatible with the legacy GEN series Master/Sync card option)  Cable length propagation delay  ±5 ns/m; Automatic cable length detection and propagation delay compensation  Synchronizes the first sample in a continuous recording for each mainframe. First sa are not recorded in the Sync mainframes defined by the cable length propagation de Signal phase shifts are not introduced by this propagation delay.  Synchronized time base Prevents frequency drift of the sample rates within each mainframe  Measured channel trigger exchange Synchronously exchanges measured channel triggers connected to the Master/Sync to bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changes that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  User action within Perception to trigger all mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GEN7IA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application.	Recording or pause active	1 minute plus 25 s per ms recording time deviation from Master time	
Cable length propagation delay ±5 ns/m; Automatic cable length detection and propagation delay compensation  First sample  Synchronizes the first sample in a continuous recording for each mainframe. First sa are not recorded in the Sync mainframes defined by the cable length propagation de Signal phase shifts are not introduced by this propagation delay.  Synchronized time base Prevents frequency drift of the sample rates within each mainframe  Measured channel trigger exchange Synchronously exchanges measured channel triggers connected to the Master/Sync to bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange  Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  Synchronous recording actions  Start/Stop and Pause a recording across multiple mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application.	User notifications while recording	Time marks on Master/Sync signal lost/restored and Master/Sync time synchronized	
First sample Synchronizes the first sample in a continuous recording for each mainframe. First sa are not recorded in the Sync mainframes defined by the cable length propagation de Signal phase shifts are not introduced by this propagation delay.  Synchronized time base Prevents frequency drift of the sample rates within each mainframe  Measured channel trigger exchange Synchronously exchanges measured channel triggers connected to the Master/Sync to bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger User action within Perception to trigger all mainframes synchronously Start/Stop and Pause a recording across multiple mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application.	Basic synchronization (backward compatible with	the legacy GEN series Master/Sync card option)	
are not recorded in the Sync mainframes defined by the cable length propagation de Signal phase shifts are not introduced by this propagation delay.  Synchronized time base Prevents frequency drift of the sample rates within each mainframe  Measured channel trigger exchange Synchronously exchanges measured channel triggers connected to the Master/Sync to bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC channel triggers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger User action within Perception to trigger all mainframes synchronously separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application.	Cable length propagation delay	±5 ns/m; Automatic cable length detection and propagation delay compensation	
Measured channel trigger exchange Synchronously exchanges measured channel triggers connected to the Master/Sync to bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  Synchronous recording actions  Start/Stop and Pause a recording across multiple mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action.  Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application	First sample	Synchronizes the first sample in a continuous recording for each mainframe. First samples are not recorded in the Sync mainframes defined by the cable length propagation delays. Signal phase shifts are not introduced by this propagation delay.	
bus between mainframes. Typically used for the sweep recording modes.  Extended synchronization (Not supported by the legacy GEN series Master/Sync card option)  Calculated channel trigger exchange  Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changes triggers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  User action within Perception to trigger all mainframes synchronously  Start/Stop and Pause a recording across multiple mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application	Synchronized time base	Prevents frequency drift of the sample rates within each mainframe	
Calculated channel trigger exchange  Synchronously exchanges real-time calculated (RTC) channel triggers between mainframes. Separate exchange required due to the longer internal delays of RTC changes that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  User action within Perception to trigger all mainframes synchronously  Synchronous recording actions  Synchronous recording actions  Start/Stop and Pause a recording across multiple mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action.  Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application	Measured channel trigger exchange	Synchronously exchanges measured channel triggers connected to the Master/Sync trigger bus between mainframes. Typically used for the sweep recording modes.	
mainframes. Separate exchange required due to the longer internal delays of RTC che triggers that were caused by the mathematics prior to establishing a trigger.  Synchronous manual trigger  User action within Perception to trigger all mainframes synchronously  Synchronous recording actions  Start/Stop and Pause a recording across multiple mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action. Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application	Extended synchronization (Not supported by the I	egacy GEN series Master/Sync card option)	
Synchronous recording actions  Start/Stop and Pause a recording across multiple mainframes, each controlled by a separate instance of Perception. Stop recording is a non-synchronous action.  Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application	Calculated channel trigger exchange	mainframes. Separate exchange required due to the longer internal delays of RTC channel	
separate instance of Perception. Stop recording is a non-synchronous action.  Synchronously records distributed data with a mix of GEN7iA/GEN3iA mainframes in Master/Sync setup while running Perception on each of the mainframes. A more typ Master/Sync setup would be to control both systems from one Perception application	Synchronous manual trigger	User action within Perception to trigger all mainframes synchronously	
Temperature Range	Synchronous recording actions		
	Temperature Range		
Operational 0 °C to 40 °C (32 °F to 104 °F)	Operational	0 °C to 40 °C (32 °F to 104 °F)	
Non-operational (Storage) -25 °C to +70 °C (-13 °F to +158 °F)	Non-operational (Storage)	-25 °C to +70 °C (-13 °F to +158 °F)	

HBK: UNRESTRICTED 20 B04376\_03\_E00\_00

### G064: 10Gbit Ethernet Card, Optical (Option, to be ordered separately)

Supports up to two 10Gbit Ethernet connections using SFP+ modules (G081 option carrier card required). Factory installed option, cannot be combined with 1-G084.

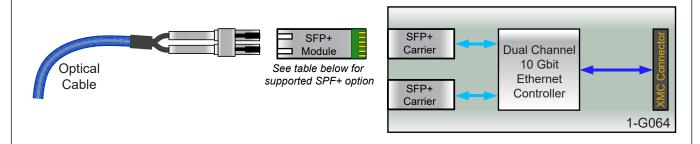


Figure 1.23: Block diagram 10Gbit Ethernet card, optical (G081 required)

ok diagram 100bit Ememet od	ira, optical (Goot requirea)		
One Ethernet option card per mainframe, cannot be combined with 1-G084			
Up to two interfaces each 10 Gbit/s optical using SFP+ modules with LC connectors			
	1 or 10 Gbit (auto detection)		
1-G065	1-G066	1-SFP-10GBIT-RJ45	
Yes	No	No	
No	Yes	No	
No	No	Yes	
850 nm	1310 nm	-	
LC	LC	RJ45	
		•	
KAB280	-	-	
-	KAB288 or KAB290	-	
-	-	CAT6A or higher	
82 m (269 ft)	10 km (6.2 mi)	100 m (330 ft)	
DHCP/Auto IP or fixed IP			
When DHCP fails, the APIPA (Automatic Private IP Addressing) setup is used similarly to Windows® PCs			
Gateway setup supported for control through VPN and/or Internet			
Not supported			
Not supported on Ethernet o	ption cards		
Not supported on Ethernet of	ption cards		
400 MB/s <sup>(1)</sup>			
0 °C to 40 °C (32 °F to 104 °	F)		
-55 °C to +85 °C (-67 °F to +	185 °F)		
	One Ethernet option card pe Up to two interfaces each 10  1-G065 Yes No No 850 nm LC  KAB280 82 m (269 ft)  DHCP/Auto IP or fixed IP When DHCP fails, the APIPA Windows® PCs Gateway setup supported for Not supported Not supported on Ethernet o Not supported on Ethernet o PTPv2 (IEEE1588:2008) car A combination of 10 Gbit and	Up to two interfaces each 10 Gbit/s optical using SFP+ mod  1 or 10 Gbit (auto detection)  1-G065  Yes  No  No  No  S50 nm  1310 nm  LC  LC  KAB280  -  KAB288 or KAB290  -  82 m (269 ft)  DHCP/Auto IP or fixed IP  When DHCP fails, the APIPA (Automatic Private IP Address Windows® PCs  Gateway setup supported for control through VPN and/or In Not supported  Not supported on Ethernet option cards  Not supported on Ethernet option cards  PTPv2 (IEEE1588:2008) can be used on a separate 1 Gbit A combination of 10 Gbit and 1 Gbit Ethernet interfaces is s	

<sup>(1)</sup> Tested using circular recording for 48 hours. Test setup uses a Windows® 7 PC with Intel i7 CPU and SSD with sustained write speeds exceeding 700 MB/s and a 10 Gbit Ethernet link.

B04376\_03\_E00\_00 21 HBK: UNRESTRICTED

### 1-G065: 10 Gbit Optical Network SFP+ module 850 nm (Option, to be ordered separately)

GEN DAQ 10 Gbit Ethernet SFP+, 850 nm Multi Mode, up to 82 m optical cable length supported, LC connector support. 10 Gbit SFP+ modules are not compatible with the 1 Gbit SFP modules.



Figure 1.24: 10 Gbit Optical Network SFP+ module 850 nm

Multi Mode SFP+ module (10GBASE-SR)	
Ordering part number	1-G065
Ethernet Speed	1 or 10 Gbit (auto detection)
Optical wave length	850 nm
Maximum cable length	82 m (269 ft) using OM3 specified optical cable (KAB280)
Connector type	LC
Temperature Range	
Operational	0 °C to 40 °C (32 °F to 104 °F)
Non-operational (Storage)	-25 °C to +70 °C (-13 °F to +158 °F)

### 1-G066: 10 Gbit Optical Network SFP+ module 1310 nm (Option, to be ordered separately)

GEN DAQ 10 Gbit Ethernet SFP+, 1310 nm Single Mode, up to 10 km optical cable length supported, LC connector support. 10 Gbit SFP+ modules are not compatible with the 1 Gbit SFP modules.



Figure 1.25: 10 Gbit Optical Network SFP+ module 1310 nm

Single Mode SFP+ module (10GBASE-LR)		
Ordering part number	1-G066	
Ethernet Speed	1 or 10 Gbit (auto detection)	
Optical wave length	1310 nm	
Maximum cable length	10 km (6.2 mi) using OS2 specified optical cable (KAB288)	
Connector type	LC	
Temperature Range		
Operational	0 °C to 40 °C (32 °F to 104 °F)	
Non-operational (Storage)	-25 °C to +70 °C (-13 °F to +158 °F)	

# 1-SFP-10GBIT-RJ45: 10Gbit Electric Network SFP+ module (Option, to be ordered separately)

GEN DAQ 10 Gbit electric Ethernet SFP+, module with RJ45 connector. Enables the mixed use of optical and electric 10Gbit Ethernet using the 1-G064 SFP+ based Ethernet card.

Note: 10 Gbit SFP+ modules are not compatible with the 1 Gbit SFP modules.



Figure 1.26: 10 Gbit Copper Network SFP module

1-SFP-10GBIT-RJ45
1 or 10 Gbit (auto detection)
RJ45
100 m (330 ft) at 1 and 10 Gbit/s
55 m (180 ft) at 10 Gbit/s 100 m (330 ft) at 1 Gbit/s
100 m (330 ft) at 1 Gbit/s (not supported at 10 Gbit/s)
0 °C to 40 °C (32 °F to 104 °F)
-25 °C to +70 °C (-13 °F to +158 °F)

### KAB280: Fiber Optic Cable MM 50/125 µm LC-LC (Option, to be ordered separately)

Standard zipcord fiber optic duplex Multi Mode patch cable

Used with 850 nm optical 1 Gbit or 10 Gbit Ethernet (1-G091 and 1-G065), Master/Sync and GN1202B cards. Typically used for fixed cable routing or LAB environments.

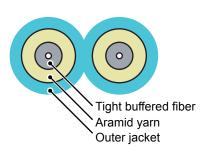




Figure 1.27: Block diagram and image

Connector type	LC-LC
Cable rating	OM3; Multi Mode, 850 nm
Core/Cladding diameter	50/125 μm
Jacket size/diameter	Typically 2 mm (0.08") single core
Jacket rating	Low-smoke zero-halogen
Attenuation	≤ 2.7 dB/km @ 850 nm
Available lengths	3, 10, 20 and 50 m (10, 33, 66 and 164 ft). For other lengths contact custom systems <sup>(1)</sup> .
Bend radius	30 mm (1.2")
Weight	Typically 14 kg/km (9 lb/1000 ft)
Operating temperature	-40 °C to +80 °C (-40 °F to 176 °F)

(1) Contact custom systems at: <a href="mailto:customsystems@hbm.com">customsystems@hbm.com</a>

# KAB288: Fiber Optic Cable SM 9/125 μm LC-LC (Option, to be ordered separately)

Standard zipcord fiber optic duplex Single Mode patch cable

Used with 1310 nm optical 1 Gbit or 10 Gbit Ethernet (1-G063 and 1-G066). Typically used for fixed cable routing or LAB environments.

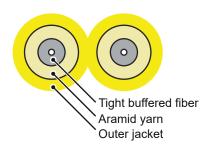




Figure 1.28: Block diagram and image

Connector type	LC-LC
Cable rating	OS2; Single Mode, 1310 nm
Core/Cladding diameter	9/125 μm
Jacket size/diameter	Typically 2 mm (0.08") single core
Jacket rating	Low-smoke zero-halogen
Attenuation	≤ 0.5 dB/km @ 1310 nm
Available lengths	2, 10, 20, 50 and 100 m (6.6, 33, 66, 164 and 330 ft). For other lengths contact custom systems <sup>(1)</sup> .
Bend radius	30 mm (1.2")
Weight	Typically 14 kg/km (9 lb/1000 ft)
Operating temperature	-40 °C to +70 °C (-40 °F to 158 °F)

(1) Contact custom systems at: <a href="mailto:customsystems@hbm.com">customsystems@hbm.com</a>

### KAB289: Robust Fiber Optic Cable SM 9/125 μm LC-LC (Option, to be ordered separately)

Heavy duty fiber optic duplex Single Mode cable

Used with 1310 nm optical 1 Gbit or 10 Gbit Ethernet (1-G063 and 1-G066). Typically used for test cell environments.

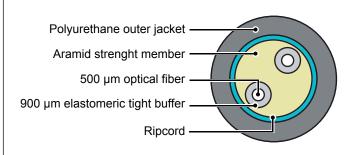




Figure 1.29: Block diagram and image

Connector type	LC-LC
Cable rating	OS2; Single Mode, 1310 nm
Core/Cladding diameter	9/125 μm
Jacket size/diameter	5.8 mm (0.23")
Jacket rating	Polyurethane, halogen free
Attenuation	≤ 0.5 dB/km @ 1310 nm
Available lengths	10, 20, 50, 100, 150 and 300 m (33, 66, 164, 328, 492 and 984 ft). For other lengths contact custom systems <sup>(1)</sup> .
Bend radius	58 mm (2.3")
Crush resistance	2000 N/cm
Weight	Typcially 32 kg/km (21.5 lb/1000 ft)
Operating temperature	-40 °C to +85 °C (-40 °F to 185 °F)

<sup>(1)</sup> Contact custom systems at: <a href="mailto:customsystems@hbm.com">customsystems@hbm.com</a>

### G070A: Torque/RPM Adapter (Option, to be ordered separately)

An external connection box to connect HBM's T12, T40B or any other RS422-based torque/RPM transducer directly to the GEN series mainframe Digital Event/Timer/Counter connector. Mainframe connection cable included.

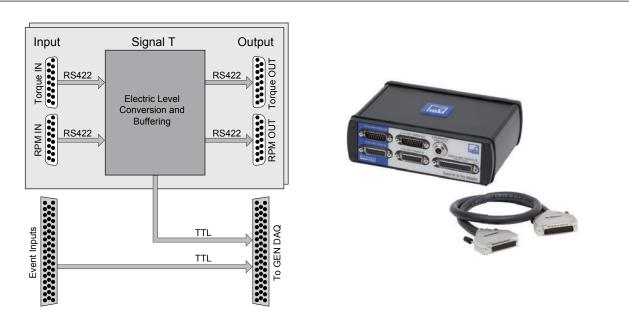


Figure 1.30: Block diagram and image

Torque sensor connection						
Number of torque sensors	2					
Torque interface support	Torque and shunt (A-Txx CON1 Torque IN & B-Txx CON1 Torque IN)					
Speed interface support	RPM, direction and reference (A-Txx CON2 Speed IN & B-Txx CON2 Speed IN)					
Signal levels	Differential RS422					
Signal termination	100 Ω					
Torque sensor loop through						
Number of torque sensors	2					
Torque interface output	Torque (A-Txx CON1 torque OUT & B-Txx CON1 torque OUT)					
Speed interface output	RPM, direction and reference (A-Txx CON2 Speed OUT & B-Txx CON2 Speed OUT)					
Output levels	Differential RS422, electronically retransmitted from input signals					
Connectors						
Digital Event/Timer/Counter	HD22 sub-D 44 pin male (connection cable included)					
Event I/O loop through connector	44 pin, female D-type connector, AMP HD-22 series (Tyco/TE Connectivity: 5748482-5)					
Event I/O loop through cable connector	44 pin, male D-type connector, HDP-22 series (Tyco/TE Connectivity: 1658680-1), to be ordered separately					
Torque, Speed/RPM interface IN	15 pin, female sub-D type connector (matches 1-KAB149-6 and 1-KAB163-6)					
Torque, Speed/RPM interface OUT	15 pin, male sub-D type connector					
Torque power input	Switchcraft L712A Matching cable connector Switchcraft 761KS17 (LD-024-1000911). Two cable connectors included					
Temperature Range						
Operational	0 °C to 40 °C (32 °F to 104 °F)					
Non-operational (Storage)	-25 °C to +70 °C (-13 °F to +158 °F)					

Note For more details, please refer to data sheet "B4229 en GEN series G070A Torque/RPM adapter".

### G072: Isolated Digital Event Adapter (Option, to be ordered separately)

An external connection box to isolate all input and output signals used on the GEN series mainframe Digital Event/Timer/Counter connector. Adapter input connector pin compatible with mainframe input connector. Mainframe connection cable included.

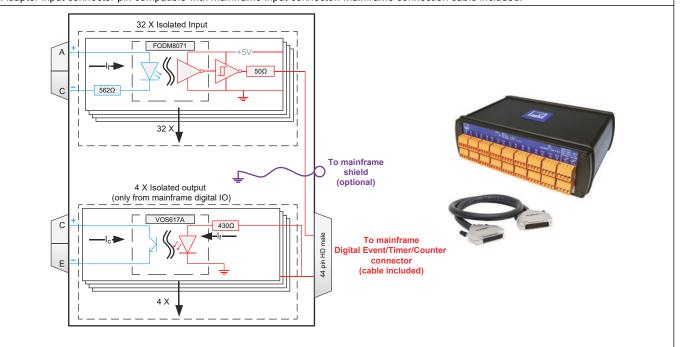


Figure 1.31: Block diagram and image

Event inputs						
Inputs	32 event channels (Anode, Cathode optocoupler with a 562 Ω series resistor )					
Isolation voltage	230 V AC RMS or DC (channel to channel and channel to chassis/earth)					
Isolation device	Fairchild FOD8071 optocoupler (or comparable)					
Switching frequency	10 MHz input block signal tested. The highest frequency supported for the system is limited by the isolator box or acquisition system, whichever is the lowest.					
Maximum propagation delay	55 ns					
Common mode transient voltage	Typically 20 kV/µs					
Input switching voltages						
Logic 0	< 1.0 V + 0.0015 A (562 Ω + R <sub>ext</sub> )					
Logic 1	> 1.3  V + 0.0050  A (562 Ω + R <sub>ext</sub> ) (+100 V when R <sub>ext</sub> = 20 kΩ)					
Maximum nondestructive voltage	1.8 V + 0.0150 A (562 $\Omega$ + R <sub>ext</sub> ) (+300 V when R <sub>ext</sub> = 20 kΩ)					
Minimum nondestructive reverse voltage	-5.0 V					
Event outputs						
Output channels	4 digital isolated output channels (open Collector, Emitter) Only supported by Digital Event/Timer/Counter connector					
Isolation device	Vishay VOS617A optocoupler (or comparable)					
Output frequency	170 kHz output signal tested.  Maximum useable frequency for the system is limited by the Isolated Digital Event Adapter or acquisition system, whichever is the slowest.					
Nondestructive control voltages						
Maximum voltage	0.007 * R <sub>ext</sub> and < 80 V					
Minimum voltage	-7.0 V					
Temperature Range						
Operational	0 °C to 40 °C (32 °F to 104 °F)					
Non-operational (Storage)	-25 °C to +70 °C (-13 °F to +158 °F)					

Note For more details, please refer to data sheet "B4232 en GEN series G072 230 Volt RMS Isolated Digital Event adapter".

### G001B: IRIG Receiver with PTP Output (Option, to be ordered separately)

External IRIG to PTPv2 convertor in a compact housing. Using the PTPv2 time source output GEN DAQ then synchronizes to IRIG time source. The solution comes as a complete package including cables, 19" rack mount kit and CD with user manual and installation instructions.

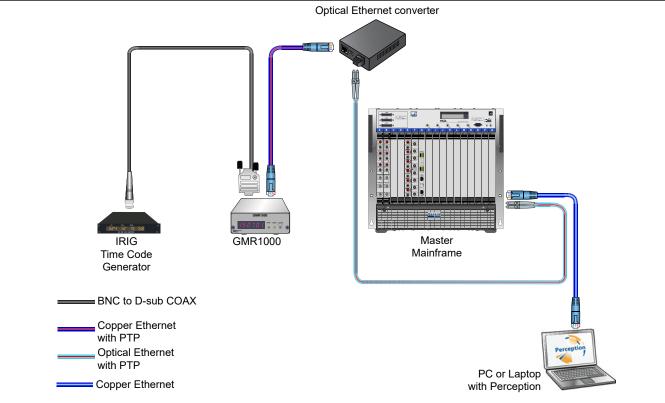


Figure 1.32: Example setup IRIG time synchronization

GMR1000					
2.5 m (8.2 ft) BNC to D-sub COAX					
4.5 m (14.8 ft) CAT6 Ethernet cable to PoE adapter 20 m (65 ft) Fiber cable standard MM LC-LC 1-KAB280-20					
Converts the electrical Ethernet signal to an optical SFP Ethernet output signal.					
2 * G091 for optical Ethernet converter and GEN DAQ mainframe optical ethernet option					
9-28 V DC					
External wall mount power supply					
164 mm (width) x 103 mm (height) x 36 mm (depth) (6.45" x 4.05" x 1.41")					
0.45 kg (16 oz)					
19", 1U height included					
IRIG-B0 (DCLS), IRIG-B1 (AM), IRIG-A0 (DCLS), IRIG-A1 (AM), IRIG-E0 (DCLS), IRIG-E1 (AM)					
< 50 μs to IRIG time (Measured on GEN DAQ mainframe)					
Capture start of recording time Synchronize master time base oscillator frequency					
< 1 min					
< 1 min plus 25 s per ms recording time deviation from IRIG time source					
PTP according to IEEE1588-2008 (1 step, End-to-End, UDP, IPv4)					
0 °C to 40 °C (32 °F to 104 °F)					
-25 °C to +70 °C (-13 °F to +158 °F)					

HBK: UNRESTRICTED 28 B04376\_03\_E00\_00

# G002B: GPS Receiver with PTP Output (Option, to be ordered separately)

External GPS time synchronization using PTPv2 network communication.

The solution comes as a complete package, including a power over Ethernet (PoE) powered GPS antenna, all required RJ45 network cable, an outdoor RJ45 network surge protector, a PoE injector, two G091 SFPs and CD with user manual and installation instructions.

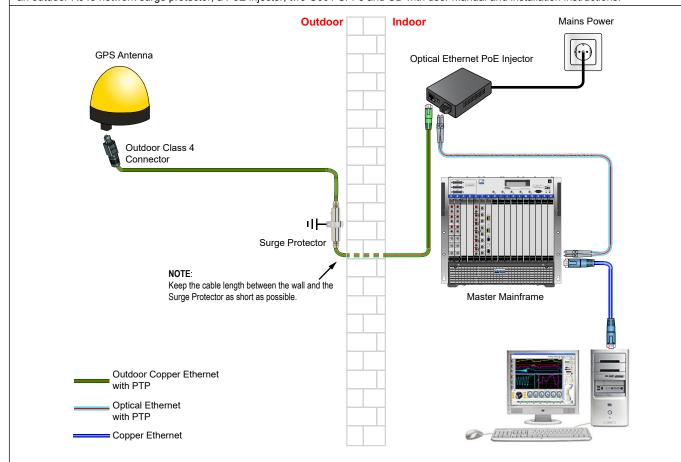
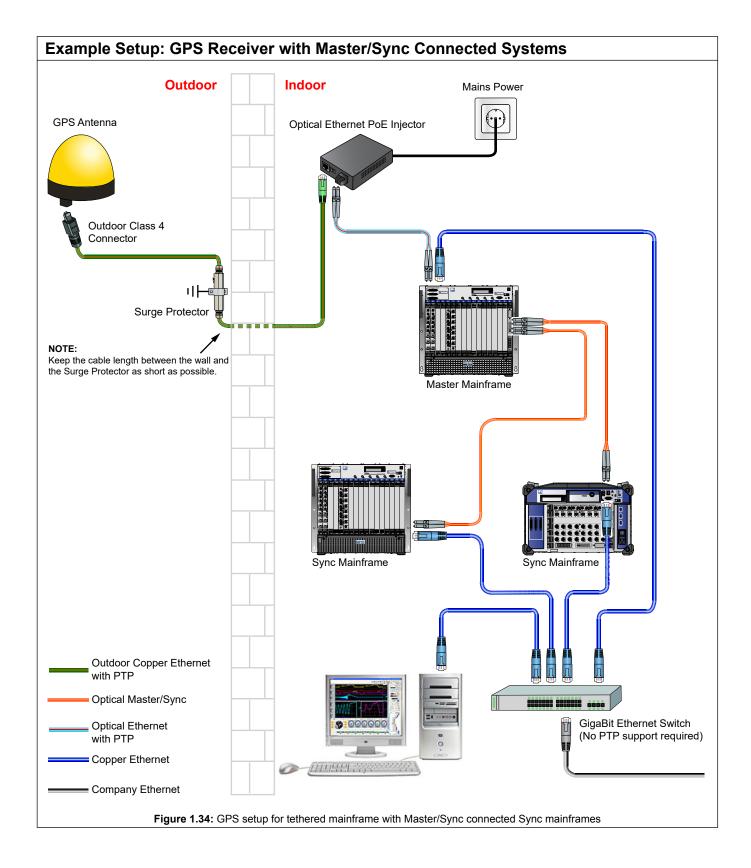
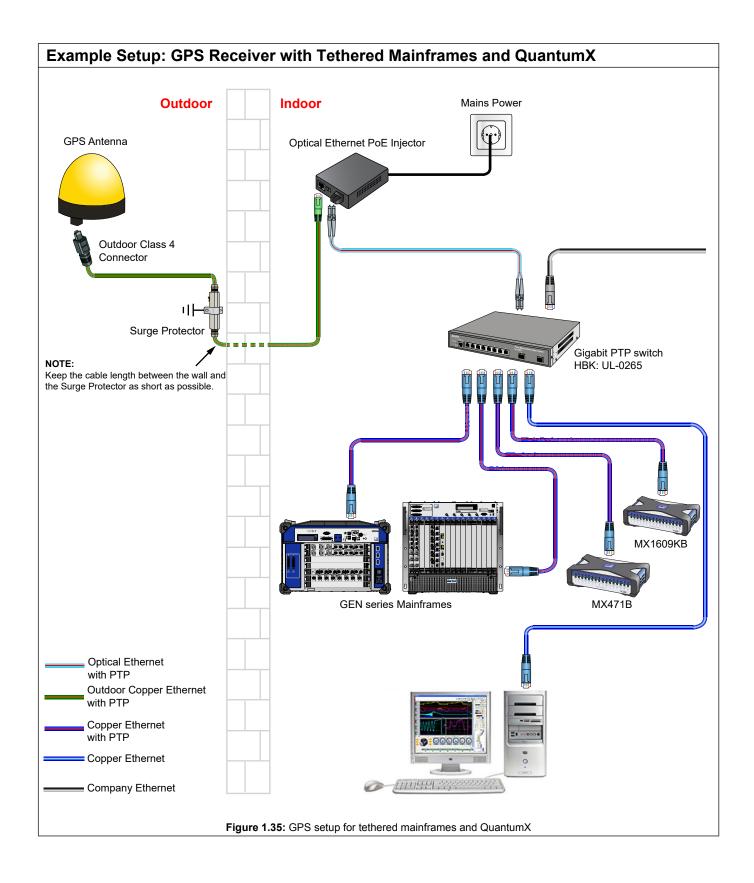


Figure 1.33: Example setup GPS time synchronization

OTMC 100						
50 m (164 ft) Outdoor CAT6 Ethernet cable to Surge Protector 20 m (65 ft) Outdoor CAT6 Ethernet cable to PoE adapter 20 m (65 ft) Fiber cable standard MM LC-LC 1-KAB280-20						
UL497B standard						
Power over Ethernet (PoE) injector. Supplies power to GPS antenna and converts the electrical Ethernet signal to an optical MM 50/125 um Ethernet output signal.						
2 * G091 for PoE injector and GEN DAQ mainframe optical ethernet option						
IEC60950-1:2005 2 Ed. +A1:2009 IEC60950-22:2005						
RJ45 waterproof connector according to IEC61076-3-106 (Variant 4)						
<150 ns to reference time (UTC) (Measured on GEN DAQ mainframe)						
Capture start of recording time Synchronize master time base oscillator frequency						
4 to 10 minutes after power on of antenna						
calization completed						
<1 min						
<1 min plus 25 s per ms recording time deviation from UTC time						
Time marks on PTP time synchronization lost/restored, Mac Address of Master						
PTP according to IEEE1588–2008 (1 step, End-to-End, UDP, IPv4)						
0 °C to 40 °C (32 °F to 104 °F						
-25 °C to +70 °C (-13 °F to +158 °F)						





# 1-USB-CAN-FD-1CHN: CAN FD Semi Real-Time Data Output (Option, to be ordered separately)

The CAN FD semi real-time data output option enables the mainframe to output periodically calculated RT-FDB results to CAN FD or CAN 2.0 bus. User selectable update rates as well as selectable calculation results to be transferred enable application specific setups. After configuration the mainframe can send results to CAN bus stand-alone without the use of Perception.

Note: At least one acquisition card inside the mainframe needs to have a 1-GEN-OP-RT-FDB option installed.

The CAN FD option connects to the mainframe's USB port and must be inserted before powering on the mainframe (No plug-and-play support).

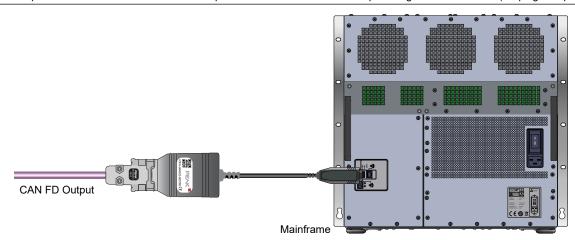


Figure 1.36: GEN17tA stand-alone CAN FD output

Included in CAN FD option					
USB to CAN FD convertor  Peak Systems: PCAN-USB FD					
CAN FD specification					
CAN support Complies with CAN specifications 2.0 A/B and FD					
CAN bit rates From 25 kbit/s up to 1 Mbit/s					
CAN FD bit rates	From 25 kbit/s up to 12 Mbit/s				
Galvanic isolation	Up to 500 V				
CAN bus connector	D-Sub, 9-pin (in accordance with CiA® 303-1)				

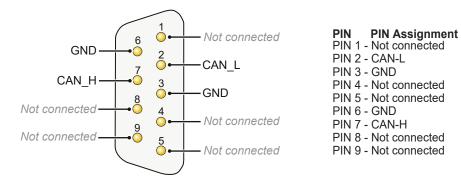


Figure 1.37: Pin assignment D-Sub

Temperature Range	
Operational	-20 °C to +60 °C (-4 °F to +140 °F)
Non-operational (Storage)	-25 °C to +70 °C (-13 °F to +158 °F)

HBK: UNRESTRICTED 32 B04376\_03\_E00\_00

<b>CAN FD Data Output Details</b>					
Plug-and-play Not supported, insert CAN option before turning the mainframe on					
Data format	Floating point values (4 bytes)				
Data endianness	User selectable				
CAN 2.0 message format	1 or 2 results per message				
CAN FD message format	1 to 16 results per message				
CAN 2.0 Message ID Resu					
CAN FD Message ID Resu	ult 1 Result 2 Result 3 Result 16				
Data package setup  1-GEN-OP-RT-FDB option required for real-time calculation of period results. User selectable data output package including RT-FDB periodic results as well as acquisition state, time and RT-FDB calculation latency.					
Maximum data package size 240 calculated results					
Data packet transfer rate 1, 2, 5, 10, 50, 100, 500 or 1000 packet updates/s					
Data packet latency  If CAN bus bandwidth allows new packet transfers start as fast as 1 ms afte been recorded. Total transfer time depends on actual CAN bus usage.					
Message 1 Message 2	Message 3 Message 4 Message n				
Figure 1.39: GEN DAQ data packet					

### **CAN FD Packet Transfer Rate and Overload Handling**

Data packet latency

Maximum packet transfer rate depends on CAN bit rate and number of results to be transferred. Adding third party CAN nodes transferring data at higher priority can negatively impact the overall transfer rate.

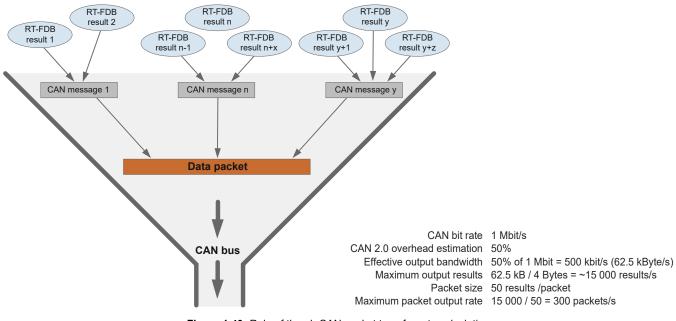


Figure 1.40	<b>):</b> Rule of thumb CAN packet transfer rate calculation
	Automatic adapted to CAN bus bandwidth

CAN bus overload handling						
Temporarily overloaded	Packet data is transmitted as soon as bus is available. Single packets might be skipped if the bus overload condition was too long.					
Continuous overloaded	Automatic packet transfer rate reduction to maximum achievable					
Extreme overloaded	This condition will only occur when multiple devices with CAN output are used with message priorities higher than the GEN DAQ messages. Initially the overload protocol is used, if extreme overloads keeps occurring, individual message of data packets might be dropped to avoid extreme aging of data values still to be transmitted. Individual message transfers will be attempted for about 100 ms per message before dropping the message					

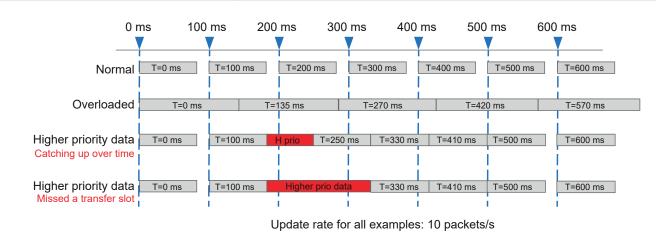


Figure 1.41: CAN output transfer rate and latency handling

Supported Acquisition Cards										
Model	Туре	Isolation	Maximum sample rate/ (not multiplexed)	Resolution	Memory/card	Analog Channels	Digital events	Timer/Counter channels	Streaming support	Slot width
GN310B	Balanced Differential/ Current	yes	2 M	18 bit	2 GB	6	16	2	fast	1
GN311B	Balanced Differential/ Current	yes	200 k	18 bit	200 MB	6	16	2	fast	1
GN610B	Balanced Differential	yes	2 MS/s	18 bit	2 GB	6	16	2	fast	1
GN611B	Balanced Differential	yes	200 kS/s	18 bit	200 MB	6	16	2	fast	1
GN815	Unbalanced Differential/ IEPE	yes	2 MS/s	18 bit	2 GB	8	16	2	standard & fast	1
GN816	Unbalanced Differential/ IEPE	yes	200 kS/s	18 bit	200 MB	8	16	2	standard & fast	1
GN840B	Bridge/IEPE/Charge/ 4-20 mA/PT100/PT1000/ Thermocouples	yes	500 kS/s	24 bit	2 GB	8	16	2	fast	1
GN1202B	Multi Mode Optical Fiber	yes	100 MS/s	(1)	8 GB	12	16	2	fast	1
GN1640B	Bridge/IEPE/Charge/ 4-20 mA/PT100/PT1000/ Thermocouples	yes	500 kS/s	24 bit	2 GB	16	16	2	fast	2
GN3210	Differential/IEPE/Charge	no	250 kS/s	24 bit	2 GB	32	16	2	standard	1
GN3211	Differential	no	20 kS/s	16 bit	200 MB	32	16	2	standard	1
GN8101B	Single-ended	no	250 MS/s	14 bit	8 GB	8	16	2	fast	1
GN8102B	Single-ended	no	100 MS/s	14 bit	8 GB	8	16	2	fast	1
GN8103B	Single-ended	no	25 MS/s	14 bit	8 GB	8	16	2	fast	1

<sup>(1)</sup> This card supports up to 12 optical fiber transmitter channels.

# **Optical Fiber Transmitter Channels**

### **Transmitter**

Every transmitter is a single channel unit. Every unit has an unbalanced differential input, amplifier, analog anti-alias filter and ADC with an optical data and control link to the receiver card. The receiver card has the recording logic, sample rate selection and memory.

Model	Receiver card	Power	Sample rate	Resolution	Isolation
GN110	GN1202B	Battery	100 MS/s	14 bit	User application defined
GN111	GN1202B	Battery	25 MS/s	15 bit	User application defined
GN112	GN1202B	120/240 V AC	100 MS/s	14 bit	1800 V RMS
GN113	GN1202B	120/240 V AC	25 MS/s	15 bit	1800 V RMS

Mainframe Feature Overview	V					
	Tethered models				Integrated models	
	GEN2tB	GEN4tB	GEN7tA	GEN17tA	GEN3iA	GEN7iA
Number of acquisition cards	2	4	7	17	3	7
Built-in TFT screen (resolution)		Not Su	pported		17" (1280x1024)	17" (1280x1024)
Built-in Windows® PC		Not Su	pported		Intel <sup>©</sup> i3, 8 GB RAM	Intel <sup>©</sup> i5, 16 GB RAM
Portable	ultra portable	portable	transporta- ble	Not Supported	portable	transporta- ble
Rack mount support (Option)			yı	es	,	
Built-in storage drive	option 500 GB	option 500 GB or 960 GB	Not Su	pported	480 GB	960 GB
Removable built-in storage drive	Not Su	pported		tion 3 EXT4	Not Supported	option 960 GB NTFS
Built-in drive continuous streaming rate	200 MB/s		350 MB/s <sup>(2)</sup>		200 MB/s	350 MB/s
1 GB Ethernet Continuous streaming rate		I.	100	MB/s		I.
10 GB Ethernet Continuous streaming rate	NS <sup>(1)</sup> 400 MB/s					
IEEE1588:2008 PTPv2 support	yes					
Digital events	up to 32	up to 64	up to 96	up to 96	up to 32	up to 96
USB ports	1	2	0			8
1 GB Ethernet (RJ45)			1			4
Master/Sync connector	SFP	option		incl	uded	
DC power output (QuantumX compliant)	NS <sup>(1)</sup>	NS <sup>(1)</sup>	30 W	NS <sup>(1)</sup>	15 W	30 W
Mechanical	GEN2tB	GEN4tB	GEN7tA	GEN17tA	GEN3iA	GEN7iA
Air filter		У	es		no	yes
Weight without acquisition cards (kg)	4.0	8.0	10.9	18.9	9	15.7
Dimensions (height / width / depth [mm])	96/375/320	133/441/345	293/448/343	450/446/517	342/436/186	350/446/386
19" Rack mount	option	included		ор	tion	
Shipping case		option		NS <sup>(1)</sup>	ор	tion
Option overview	GEN2tB	GEN4tB	GEN7tA	GEN17tA	GEN3iA	GEN7iA
IRIG time synchronization (G001B)			op	tion		
GPS time synchronization (G002B)			op	tion		
Option carrier card support (G081)			op	tion		
Master output card (G083)	option					
10 GB Ethernet (G064)	NS <sup>(1)</sup> option					
EtherCAT® real-time output	NS <sup>(1)</sup> option Not Supported			pported		
CAN FD semi real-time output	option Not supported					
Software	GEN2tB	GEN4tB	GEN7tA	GEN17tA	GEN3iA	GEN7iA
Included Perception package	Standard Advanced			Advanced	Enterprise	
GEN DAQ API remote control	Standard supported NS <sup>(1)</sup> NS <sup>(1)</sup>			NS <sup>(1)</sup>		
Perception API remote control	Standard supported					
Perception CSI (custom special software)			op	tion		

<sup>(1)</sup> NS: Not supported

<sup>(2)</sup> **Note**: Please check specific storage option for maximum continuous streaming rate.

Features	Viewer (no copy protection)	Viewer Enterprise	Standard (no copy protection)	Advanced	Enterprise
True 64 bit support	<b>✓</b>	<b>✓</b>	<b>V</b>	✓	<b>✓</b>
Basic review, y/t and x/y displays	<b>✓</b>	<b>*</b>	<b>V</b>	✓	<b>✓</b>
Horizontal, vertical and slope cursors	<b>✓</b>	<b>*</b>	<b>✓</b>	✓	<b>✓</b>
Trace and display markers	<b>✓</b>	<b>*</b>	<b>✓</b>	✓	<b>✓</b>
Interactive waveform calculator	<b>✓</b>	<b>*</b>	<b>V</b>	<b>V</b>	<b>V</b>
Interactive user keys	<b>✓</b>	<b>*</b>	<b>V</b>	<b>✓</b>	<b>V</b>
Quick report to Microsoft® Word and Excel	<b>✓</b>	<b>*</b>	<b>✓</b>	✓	<b>✓</b>
Automation and log-file	<b>✓</b>	<b>*</b>	<b>✓</b>	✓	<b>V</b>
Export to ASCII, Excel, imPression, RTPro, TEAM data	<b>*</b>	<b>*</b>	<b>✓</b>	<b>✓</b>	<b>*</b>
Analysis functions/Formula Database	×	<b>✓</b>	×	✓	<b>V</b>
Advanced Report	×	<b>✓</b>	×	✓	<b>V</b>
Advanced Export adds 15 additional formats MATLAB, DIAdem, Flexpro, Famos, UFF58 etc.	×	<b>*</b>	×	<b>✓</b>	<b>✓</b>
Synchronized Video Playback	×	✓	×	✓	<b>V</b>
Multiple Workbooks (Monitors)	×	✓	×	✓	<b>V</b>
Information sheet to add recording meta data	×	<b>✓</b>	×	✓	<b>V</b>
Single mainframe control	×	×	<b>√</b>	✓	<b>√</b>
Multiple mainframe control <sup>(1)</sup>	×	×	×	×	<b>✓</b>
Macro editor for user keys and automation	×	<b>✓</b>	×	×	<b>√</b>
Basic FFT	×	<b>✓</b>	×	×	<b>✓</b>
Sensor Database	×	<b>✓</b>	×	×	<b>✓</b>
User/Definer Mode	×	<b>✓</b>	×	×	<b>V</b>
Application packages					
Custom Software Interface	×	Cost option	×	Cost option	Cost option
STL Analysis (Short-Circuit Testing Liaison methods)	×	Cost option	×	Cost option	Cost option
HV-IA Lightning, Switching and Current impulse analysis (IEC60060-1 and IEC61083-2)	×	Cost option	×	Cost option	Cost option
eDrive electrical motor/inverter/generator and drive analysis	×	Cost option	×	×	Cost option

<sup>(1)</sup> The maximum number of mainframes Perception can control is calculated by using 25% of PC memory divided by 50 MB FIFO required per mainframe. Minimum suggested configuration is a PC with 64 bit Windows® and 8 GB of memory.

### **Perception Remote Control (Free of Charge)**

Perception remote control is based on DCE/RPC network communication standards (<u>Distributed Computing Environment/Remote Procedure Calls</u>, free of charge). The source code supplied by HBM can be compiled on many different operating systems. For ease of use in the Microsoft®.NET environment a COM interface is created on top of the basic DCE/RPC interface. An extensive help file is available to explain interface calls offered in this API.



Figure 1.42: Functional diagram DCE/RPC

Functions	Control Perception software from an external computer/application on Windows®, Linux, Unix or Mac OS X
COM interface	All RPC commands have a COM wrapper for easier Windows® software integration
Available basic commands	Load and save Perception setup files, Setup Recording, set and review Hardware Settings, Start/Stop/Pause/Trigger, monitor Live data
Examples (free of charge)	C++ and C# getting started example programs supplied for Windows®, source code included. Unsupported Linux getting started example by request only.
LabVIEW™ integration (free of charge)	LabVIEW™ RPC/COM getting started examples available on www.hbm.com
DIAdem™ integration (free of charge)	DIAdem™ RPC/COM getting started examples available on www.hbm.com

### **GEN DAQ API (Free of Charge)**

GEN DAQ API is based on JSON-RPC 2.0 network communication standards. The source code supplied by HBM can be compile on many different operating systems. For ease of use in the Microsoft®.NET environment source code for a COM interface is supplied as well. An extensive help file is available to explain interface calls offered in this API.

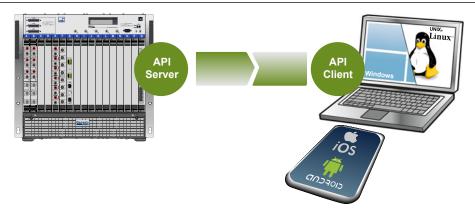


Figure 1.43: Functional diagram GEN DAQ API

rigure 1.40.1 uncuonal diagram OEN DAG Al 1				
Functions	Basic stand-alone control of GEN DAQ systems. Live data retrievable using EtherCAT® output. Setup and control using Perception Standard (Free of Charge)			
Available basic commands	Start, Stop, Pause and Preview recording Trigger sweep based recording Load and Save system power on settings Delete recordings Change sample rate			
API client Source code (free of charge)	C source code (Useable with any ANSI C compiler) Microsoft®.NET source code (Usable with any .NET compiler)			
Communication technology used	Basic socket communication on TCP/IP level. JSON-RPC 2.0 calls are build and created in our source code driver. No additional OS or pre-installed JSON-RPC 2.0 dependences.			

# **PNRF Recording File Reader (Free of Charge)**

HBM maintained file reader to read the proprietary PNRF format. (Perception Native Recording File) Integrated by several industry standard analysis package suppliers. Available for all  $3^{rd}$  party software developers.

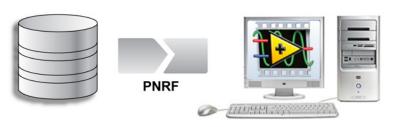
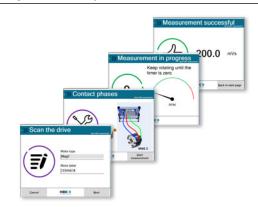


Figure 1.44: Functional diagram PNRF Reader

Read PNRF, NRF and LRF recording files directly in your own application
The PNRF reader comes as a COM interface and can be used from any application or programming language which supports COM automation
Installs PNRF dll's and supplies Visual Basic, C# and C++ getting started examples
PNRF SDK integrated and available directly from HBM nCode
PNRF SDK installs both MATLAB® PNRF reader and getting started examples
PNRF SDK integrated and available directly from National Instruments
PNRF SDK integrated and available directly from National Instruments
PNRF SDK integrated and available directly from Weisang GmbH
PNRF SDK integrated and available directly from AMS
PNRF SDK integrated and available directly from Intespace

# Perception CSI (Customer Software Interface)



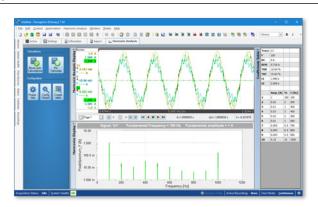


Figure 1.45: Perception CSI examples BackEMF (left) Harmonic analysis (right)

Functions	Create software extensions inside the Perception software by adding CSI user sheets, custom automation and extended analysis functions. Basic Windows C# sheet template included. Available for all languages that support Microsoft®.NET 4.
Available basic controls & commands	Access to every Perception part: Start/Stop/Pause and Trigger, Start Manager, Acquisition System, Hardware Settings, Displays, Meters, User Tables, Formulas, Calculations, Data Manager, Data Sources, User variables, Notifications, Logging, Conversion Functions, Automation Actions, Sheet Manager and more, to create a dedicated application GUI that hides the entire Perception standard GUI.
Examples (free of charge)	C# getting started example programs supplied, source code included

# **Perception and eDrive Training Program**



Figure 1.46: Perception on-site training

HBM offers paid professional training and support programs on all API interfaces (PNRF reader, RPC and CSI). Training programs are based on C#, are on-site or are at a central HBM location. On-site training can be specific for each customer. Support can be the development of a fully customized software application or answering questions from software engineers.

S-TRAIN1-GEN_PERC	First day on-site basic training on GEN DAQ/PERCEPTION.  Example content: Basic usage, hardware setup, acquisition.  Training can be customized for specific training needs.
S-TRAIN2-GEN_PERC	Second day on-site enhanced training on GEN DAQ/PERCEPTION. Training can be customized for specific training needs.
S-TRAIN1-eDRIVE	First day on-site basic training on eDrive application specifics.  Example content: Basic usage, hardware setup, acquisition.  Training can be customized for specific training needs.
S-TRAIN2-eDRIVE	Second day on-site enhanced training on eDrive application specifics.  Training can be customized for specific training needs.
1-PERC-CSI-TRAIN	Two day on-site Perception CSI training for software programmers During the training software programmers learn how to get started using the CSI template, make changes to the Perception user interface, to add new mathematical routines to the Formula Database or to add User Keys etc. The exact training details can be fully customized to the programmers needs including reviews and examples how to create the exact CSI changes of choice.  Basic Microsoft® Visual Studio software C# programming skills are required before joining this training.
1-PERC-CSI-PROJ	More dedicated detailed training is available on request.  One day eMail/Phone support for Perception CSI or RPC programmers. Get support from a HBM senior software engineer. Support can range anywhere from answering "how-to" question, assisting in analysing any kind of (performance) issue to generating basic getting started example code fragments.

HBK: UNRESTRICTED 40 B04376\_03\_E00\_00

Ordering Information				
Article		Description	Order No.	
GEN17tA		GEN17tA robust, desktop/rack mountable transient recorder and data acquisition system. Includes 17 slots for acquisition and option carrier cards, a 1 Gbit copper Ethernet interface, a master time base and a single Master/Sync connector. Has a standard continuous streaming rate (to a suitable PC) of 100 MB/s and supports fast data streaming.	1-GEN17tA	

Solid State Drive (Option, to be ordered separately)			
Article		Description	Order No.
Removable solid state drive	ECT4 removable seld state disk  Dari states care and state disk  And states care and state disk  ECT4 removable seld state disk  And states care and state disk  And states care and states ca	GEN7tA/GEN17tA specific Linux EXT4 preformatted RAID 0 Solid State Drive array mounted in removable drive carrier. Unformatted capacity 960 GB, 350 MB/s continuous streaming rate. Sweep storage rate depends on sweep length and number of channels. Short sweeps are stored more slowly due to administration overhead.	1-G079

GEN17tA Accessories (Options, to be ordered separately)				
Article		Description	Order No.	
GEN17tA air filter		GEN17tA air filter. Regular replacement recommended.	1-G085	

Article	Description	Order No.
2 Gbit Optical SFP module MM 850 nm	GEN DAQ 2 Gbit Ethernet SFP, 850 nm Multi Mode, up to 600 m optical cable length supported, LC connector support.  Not compatible with the 10 Gbit SFP+ modules.  Operating temperature: -20 °C to +60 °C	1-G091
1 Gbit Optical Network SFP module 1310 nm	GEN DAQ 1 Gbit Ethernet SFP, 1310 nm Single Mode, up to 10 km optical cable length supported, LC connector support.  Not compatible with the 10 Gbit SFP+ modules.  Operating temperature: -10 °C to +60 °C	1-G063
10 Gbit Optical Network SFP+ module 850 nm	GEN DAQ 10 Gbit Ethernet SFP+, 850 nm Multi Mode, up to 82 m optical cable length supported, LC connector support.  10 Gbit SFP+ modules are not compatible with the 1 Gbit SFP modules.  Operating temperature: 0 °C to +40 °C	1-G065
10 Gbit Optical Network SFP+ module 1310 nm	GEN DAQ 10 Gbit Ethernet SFP+, 1310 nm Single Mode, up to 10 km optical cable length supported, LC connector support. 10 Gbit SFP+ modules are not compatible with the 1 Gbit SFP modules. Operating temperature: 0 °C to +40 °C	1-G066
10 Gbit Copper Network SFP+ module	GEN DAQ 10 Gbit Ethernet SFP+, Copper, up to 30 m cable length supported, RJ45 connector support.  Note: 10 Gbit SFP+ modules are not compatible with the 1 Gbit SFP modules.  Operating temperature: 0 °C to +40 °C	1-SFP-10GBIT- RJ45

Fiber Optic Cables (Options, to be ordered separately)				
Article		Description	Order No.	
Fiber cable MM LC-LC		GEN DAQ standard zipcord fiber optic duplex Multi Mode 50/125 µm cable, 3.0 dB/km loss, LC-LC connectors, aqua, ISO/IEC 11801 type OM3. Typically used for fixed cable routing or LAB environments.  Lengths: 3, 10, 20 and 50 meters (10, 33, 66 and 164 ft)	1-KAB280-3 1-KAB280-10 1-KAB280-20 1-KAB280-50	
		Used with 850 nm optical 1 Gbit or 10 Gbit Ethernet (1-G091 and 1-G065), Master/Sync and GN1202B cards.		
Fiber cable SM LC-LC		GEN DAQ standard zipcord fiber optic duplex Single Mode 9/125 µm cable, 0.5 dB/km loss, LC-LC connectors, yellow, ISO/IEC 11801 type OS2. Typically used for fixed cable routing or LAB environments.  Lengths: 2, 10, 20, 50 and 100 meters (6.5, 33, 66, 164 and 328 ft)	1-KAB288-2 1-KAB288-10 1-KAB288-20 1-KAB288-50 1-KAB288-100	
		Used with 1310 nm optical 1 Gbit or 10 Gbit Ethernet (1-G063 and 1-G066).		
Robust fiber cable SM LC-LC		GEN DAQ heavy duty fiber optic duplex Single Mode 9/125 μm cable, 0.5 dB/km loss, LC-LC connectors, black, ISO/IEC 11801 type OS2. Typically used for test cell environments. Lengths: 10, 20, 50, 100, 150 and 300 meters (33, 66, 164, 328, 492 and 984 ft)	1-KAB289-10 1-KAB289-20 1-KAB289-50 1-KAB289-100 1-KAB289-150 1-KAB289-300	
	35	Used with 1310 nm optical 1 Gbit or 10 Gbit Ethernet (1-G063 and 1-G066).		

 $\textbf{Note} \qquad \textit{Other fiber cable lengths can be ordered from custom systems} \textit{ at: } \underline{\textbf{customsystems@hbm.com}}$ 

Option Carrie	Option Carrier Card and Add-ons (Options, to be ordered separately)				
Article		Description	Order No.		
Option carrier card		The option carrier card enables the use of two option cards within the GEN2tB, GEN3iA, GEN4tB, GEN7iA, GEN7tA and GEN17tA mainframes. Multiple option carrier cards are supported.  Option cards enable the use of synchronization, field busses and 10 Gbit Ethernet.  Operating temperature: 0 °C to +40 °C	1-G081		
EtherCAT <sup>®</sup> card		Factory installed, option carrier card (G081) required. Real-time data transfer using industrial digital communication standard EtherCAT®. The card supports a single EtherCAT® slave node using two RJ45 connectors. Fixed ESI configuration with SDO and PDO data output that can be configured by the user. PDO data rates up to 1 kS/s. GEN series mainframe setup and control using EtherCAT® communication not supported. Maximum of one EtherCAT® card per mainframe. Operating temperature: 0 °C to +40 °C	1-G082		
Master output card		Factory installed, option carrier card (G081) required. The Master output card supports the use of four Sync mainframes. Up to two Master output cards are supported per option carrier card. Multiple option carrier cards supported per mainframe. Compatible with Master/Sync card (1-G040) and mainframe Master/Sync. Operating temperature: 0 °C to +40 °C	1-G083		
10 Gbit Ethernet card, optical		Factory installed, option carrier card (G081) required.  The optical 10 Gbit Ethernet card adds up to two extra 10 Gbit Ethernet network interfaces to a GEN DAQ series mainframe. Supports up to 400 MB/s continuous data transfer from the GEN DAQ mainframe to a suitable PC. Requires a 10 Gbit optical network SFP+ module.  Requires one or two 10 Gbit network SFP+ module.  Can not be used together with 1-G084.  Operating temperature: 0 °C to +40 °C	1-G064		

B04376\_03\_E00\_00 43 HBK: UNRESTRICTED

CAN/CAN FD (Options, to be ordered separately)			
Article		Description	Order No.
USB to CAN FD converter		The CAN FD semi real-time data output option enables the mainframe to output periodically calculated RT-FDB results to CAN FD or CAN 2.0 bus.  User selectable update rates as well as selectable calculation results to be transferred enable application specific setups. After configuration the mainframe can send results to CAN bus stand-alone without the use of Perception.  The CAN FD option connects to the GEN DAQ mainframe's USB port and must be inserted before powering on the mainframe (No plugand-play support). At least one acquisition card inside the mainframe needs to have a 1-GEN-OP-RT-FDB option installed to enable the use of the CAN FD output.  Operating temperature: -20 °C to +60 °C	1-USB- CANFD-1CHN

General Accessories (Options, to be ordered separately)			
Article		Description	Order No.
I/O BNC Breakout cable		BNC breakout cable for direct BNC cable connection to the 9-pin D-sub I/O connector	1-KAB2132-0.5
Torque/RPM adapter		Converts the differential signaling used by HBM torque transducers to TTL signal levels used by the Timer/Counter A and B available on the Digital Event/Timer/Counter connector of GEN DAQ mainframes. Both Torque and Speed are interfaced separately for 2 torque sensors. Event output connected to Shunt control. All remaining event TTL signals available on output connector. Comes with 0.7 m (2.3 ft) cable to connect adapter to the mainframe. Torque transducer cables not included.	1-G070A
Isolated digital event adapter		230 V RMS Isolated Digital Event adapter. Supports 32 channel to channel isolated digital event inputs. The inputs can either be used to connect to the GEN series mainframes that support the Digital Event/Timer/Counter connector. Input connectors and cable to connect to the GEN series mainframe are included.	1-G072

Article		Description	Order No.
IRIG to PTPv2 convertor	GMR1000    15 0 7 0   PTP NTP LOCK   Section   PTP NTP LOCK   PTP	External IRIG to PTPv2 convertor in a compact housing. Using the PTPv2 time source output GEN DAQ then synchronizes to IRIG time source. The solution comes as a complete package including cables, 19" rack mount kit and CD with user manual and installation instructions.	1-G001B
GPS to PTPv2 receiver		External GPS time synchronization using PTPv2 network communication.  The solution comes as a complete package, including a power over Ethernet (PoE) powered GPS antenna (OTMC 100i), a 50 m (164 ft) IP67 CAT6 outdoor RJ45 network cable, an outdoor RJ45 network surge protector (PD-OUT/SP11), a 20 m (65 ft) CAT6 RJ45 network cable, a RJ45 to Optical SFP convertor with PoE injection on the RJ45 network, two G091 SFPs (For GEN DAQ SFP network and the SFP convertor), a KAB280-10 optical cable and CD with user manual and installation instructions.	1-G002B
Gbit PTP ethernet switch	TO THE REAL PROPERTY OF THE PARTY OF THE PAR	UL-0265 is a 10/100/1000 Mbps network switch with IEEE1588:2008 PTPv2 time synchronization and PoE (Power over Ethernet) support.  The switch is pre-configured for plug and play use on both IPv4 PTP used by GEN DAQ systems as well as PoE output for 8 devices. For systems using IPv6 PTP, the switch can be reprogrammed.  UL-0265 has a built-in 100 to 240 V AC, 50 to 60 Hz mains power supply.	UL-0265

Software (Options, to be ordered separately <sup>(1)</sup>				
Article		Description	Order No.	
Perception Advanced		For setup and control of a single GEN series mainframe. Includes real-time live and recorded data review using y/t and x/y displays. Y/t displays support vertical, horizontal and slope cursors, trace and display markers as well as an interactive waveform calculator. On top Perception allows synchronized video playback. For data analysis Perception supports interactive user keys, Formula Database with waveform and math calculators. To create a report of the recorded and analysis data Perception supports adding additional meta data describing your test details, quick report to Microsoft Word® and Excel®, an advanced built-in report engine. If analysis in third party software is preferred 20 export format (Including MATLAB, DIAdem, MDF4/ASAM, UFF58 and more) are supported. For automated analysis, reporting or data exports Perception supports extensive automation and result logging features. Perception supports 64 bit versions of Windows® 10.	1-PERC-AD-01	
Perception Enterprise		Perception Advanced with additionally: Macro editor, Basic FFT, Sensor Database, User Definer Mode and Multi Mainframe Control.	1-PERC-E64-01	
Perception Viewer Enterprise	the percentage distings.	Same as Perception Enterprise without mainframe setup and control.	1-PERC-VA-01	
CSI Interface	Mosourement successful  Mosourement successful  Mosourement successful  Some the drive  Some the drive  The successful  The su	License extension to develop and use customer specific created user interface and/or mathematical / evaluation software extensions. HBM offers the service of custom made Perception extensions. An experienced software engineer will contact the end user and create a requirements document. A project quote will be made based on the agreed requirements.	1-PERC-OP- CSI-01	
STL Analysis		Special analysis routines in accordance with the STL standard used in LV, MV and HV labs. Includes import of TDG data (Test Data Generator) for verification. Includes HighPower/HighVoltage automated analysis. Evaluates data from NoLoad, ShortCircuit, Capacitive and Synthetic tests of HV/MV switchgear devices.	1-PERC-OP- STL-01	
HV-IA		High Voltage Impulse Analysis option; evaluates Lightning, Switching and Current impulses; designed in accordance with IEC60060-1 and IEC61083-2 requirements. Allows for evaluation with new k-factor method.	1-PERC-OP- HIA-01	
eDrive		Allows for easy and application oriented setup and efficiency calculations of electrical inverter/drive tests with minimum interaction. Requires Perception Enterprise.	1-PERC-OP- EDR-01	

<sup>(1)</sup> Software options are also sold in a package with multiple single seat licenses and multiple seat network license.

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